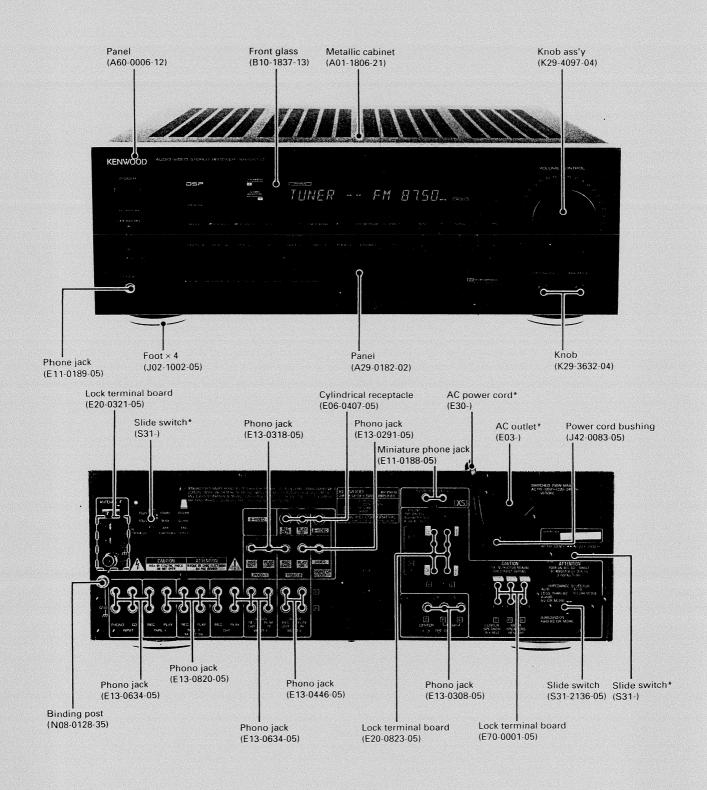
AUDIO-VIDEO STEREO RECEIVER

KR-V9030

SERVICE MANUAL

KENWOOD

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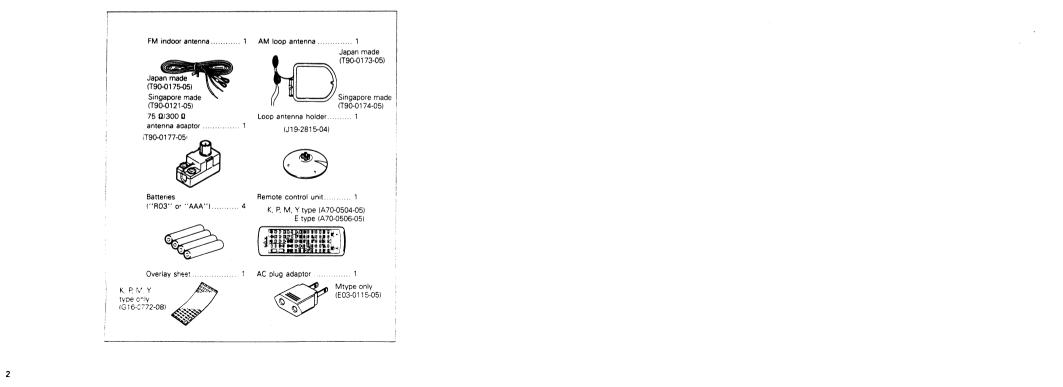
KR-V9030

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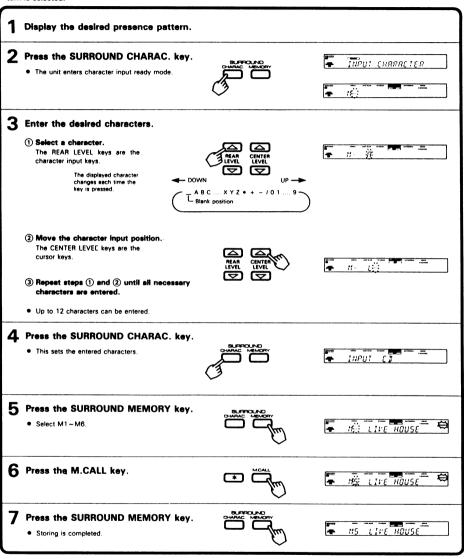
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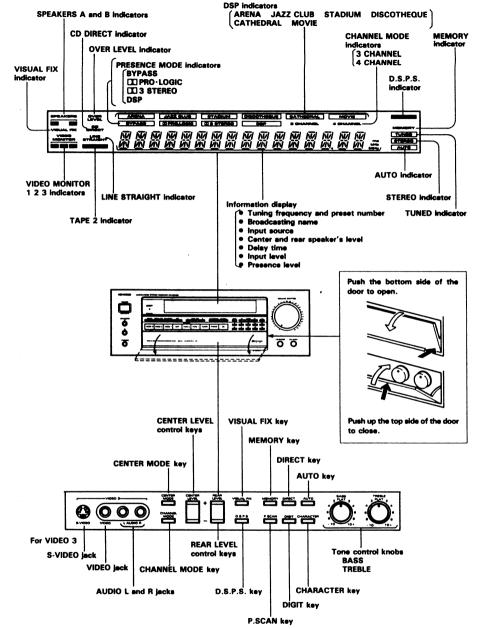
ACCESSORIES





This function lets you freely create a name for a stored presence pattern and displays the name whenever that presence pattern is selected.





Note

If no characters are entered, the previous name is stored as is.

Refer to instruction manual for detail

■ Operation of remote control unit

ON/OFF

med under them

For the TV and VIDEO keys, specific

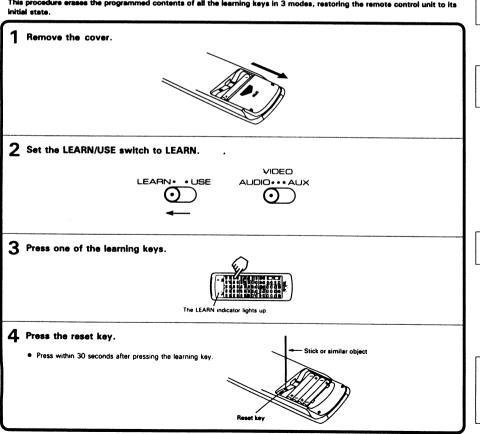
control signals should be program-

The supplied remote control unit has two operation modes: USE, for operating various components in your system, and [LEARN], for programming ("learning") the remote control functions of other AV equipment. There are three USE modes. One is AUDIO mode, for operating KENWOOD system audio components, another is VIDEO mode, for operating AV components, and the third is AUX mode, for operating other optional equipment.

LEARN mode is used to program the functions of other AV components into this remote control unit. This lets you perform the functions of several remote control units using a single remote control unit.

■ To erase all of the programmed contents

This procedure erases the programmed contents of all the learning keys in 3 modes, restoring the remote control unit to its



LEARN/USE switch Mode switch VIDEO Set to USE for operating various components in AUDIO mode. AUDIO • • AUX Set this switch according to the type VIDEO mode, or AUX mode. of component you plan to operate Set to LEARN when programming function of other remote (\bullet) control unit. LEARN. · USE TRANSMIT indicator **LEARN** indicator Lights up while the remote control Blinks or lights steadily during signal is being transmitted. programming procedure. **-**3 0 0 0 0 Numeric kevs When the CD source is selected, **4 6 1 3 4** these keys can be used as the numeric keys of the CD player. 1 0 0 4 **6** When the TUNER source is selected, they can be used as the numeric keys of the tuner. When the mode switch is set to VIDEO, a LD player (manufactured by KENWOOD) can also be operated. **ॼ ॼ ॼ** ॼ How to enter numerals: For 23 press + 10 twice and 3 國 区 区 For 40 press + 10 four times and 0 Input select keys PMO-LOGIC 3 STEREO These keys operate the input selector. **VOLUME CONTROL keys** Adjust the volume. During operation, the VOLUME CONTROL on the front panel turns and the indicator on the knob blinks at high speed. POWER keys MUTE key Turn the power of components Press to reduce the volume tem-

KENWOOD

porarily.

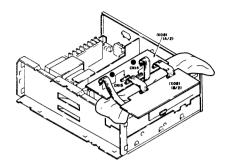
During operation, the indicator on the

VOLUME CONTROL knob blinks.

DISASSEMBLY FOR REPAIR Removing the front panel, subpanel and X14 PC hoard

Removing the X08 PC board

- Place the PC board <X13> in its original position
 Remove the shield plate, and put the PC board on top of the set
- 12. Pass the cord between the two PC boards and connect it to CN14 and CN15 ()

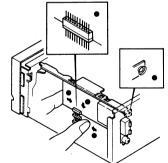


Removing the X05 PC board

- 13. Remove the three screws ()
- 14. Slide the antenna terminal upwards (19).
- 15. Remove the connector ().

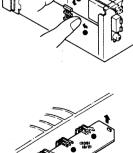


- 16. Push the connector in the direction of the arrow ()
- 17. Push the PC board in the direction of the arrow ().
- 18. Insert the PC board ().
- 19. Insert the projection into the hole in the rear panel ().



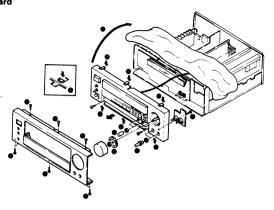
Removing the X09 (B/2) PC board

Desolder the final transistor leads, and remove the PC board ().

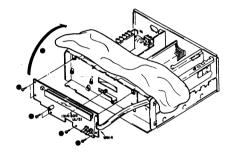


Removing the front panel, subpanel and X14 PC board • Remove the case

- 1. Remove the eight screws () from the front panel.
- 2. Remove the volume knob (2)
- 3. Remove the four knobs (3).
- 4. Remove the six hooks (4) and remove the subpanel.
- 5. Remove the screw and remove the PC board ()
- 6. The subpanel can be put on the top of the set ()

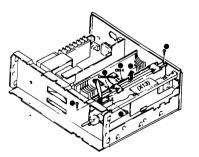


- 7. Remove the seven screws () and draw out the WH4 cord
- 8. The PC board can be put on the top of the set (3).



Removing the DSP PC board board.

- After removing the PC board <X13>
- Remove the three screws (
- 10. Disconnect the cord between CN15 and CN14().
- 11. Remove the DSP board with the set ().



BLOCK DIAGRAM

(XO5) (E) : WO2-0700-05 (X14) OTHER: W02-0699-05 REMOTE SENSOR FRONT - END FM IF AME FM DET ANT AM FL DISPLAY KEY BOARD MPX LPF 163,4 03~19 101 EEP ROM FL DRIVE SUB .- COM AM DET POWER STAND BY 102 PLL LPF MAIN J-COM TAPE 2 RESET PLAY: 200mV/47KΩ REC: 200mV/3.3KΩ C4 0.047F/ 5.5 (AUDIO) REC PLAY BACKUP CON. 2.5mV/47K 1631 PHONO 200mV/47KΩ O. LEVEL SYSTEM CONTROL µ - COM (X00) DATA TAPE I PLAY (O SYSTEM BUSY DAT PLAY (0) VIDEO I PLAY (0 ICE CD. DIR 32.25V/8A (X09) Q7~14 (X85) Q1~16 VIDEO 2 PLAY (0) SP. A FRONT L or R MASTER IC 4 (NORMAL BYPASS TONE AMP VIDEO 3 PLAY SP. B SPEAKERS T. MUTE 5. MUTE ZOOMV/3.3KD TAPE I REC 105 DAT REC (O BASS TREBLE PHONES VIDEO I REC (O (X14) ELECTRIC VIDEO 2 REC (0 VOL. (X09) Q17~19 (X85) Q21~25 [(X13) 22.8V/8Ω IN / OUT (VIDEO) + CENTER 1015 ICII 101 VIDEO I IN O ADC OZZ CENTER PREOUT VIDEO 2 IN 1 1 5 1 * 1025~28 ELECTRIC VOL. VIDEO 3 IN (O TIMING LOGIC VIDEO I OUT (O 11/2) LPF 10.9V/8A VIDEO 2 OUT (0 1 C 29 (X00) Q3~5 (X85) Q31~35 REAR MONITOR OUT (O) 1027,28 AUTO PRO-LOGIC ADAPTIVE MATRIX TK LPF SPEAKER DSP D - RAM 256K IC 2 (48KHz SAMPLING RATE) VIDEO 2 IN S-C R. MUTE REAR PREOUT VIDEO 2 IN S-Y OVER LEVEL 1 C 3 O (X00) VIDEO 3 IN S - C DSP ... COM 5.6V REGULATOR OVER LEVEL VIDEO 3 IN S-Y SYSTEM CONTROL #-COM To (XOB) [C31 Te (XOB) 1C31 VIDED 2 OUT S-C S-V. MUTE AC IN VIDEO 2 OUT S-Y 059 MONITOR S - C ANALOG SIGNAL (XO8) (AVR) (XOO) 955 MONITOR S-Y DIGITAL SIGNAL 80 --- 40 SURROUND (DSP) OFF -- ON

CIRCUIT DESCRIPTION

Description of functions

1. Feature

a. Input selector

VIDEO 1 and VIDEO 2 allow recording and playback of audio and video, but VIDEO 3 allows playback only.

 Surround function (Available only when the rear panel switch is set to SURROUND ON.)

There are six surround modes: ARENA, JAZZ CLUB, STADI-UM. DISCOTHEQUE, CATHEDRAL, and MOVIE.

Surround memory can be set for each of surround modes, and character of maximum 12.

c. Automatic function

The following operations are possible by connecting component units to the receiver with control lines.

- 1 Switching the amplifier selector by starting.
- 2 Starting a unit by selecting it using amplifier selector.
- 3 When a deck is in record mode, the amplifier select keys and play codes for other units become invalid.

DSP surround reproduction

1. Speaker modes

There are two modes for the KR-V9030 DSP surround reproduction, front 3-channel mode and 4-channel mode, depending on e speakers to be placed.

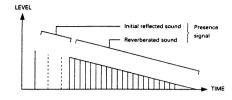
(1) Front 3-channel mode (Surround reproduction with the main left, right, and center speakers)

Since no rear speakers are used, the system configuration is simple, but the sound field is narrow between the
right and left speakers if only indirect sound is applied.
The indirect sound signal in the band that has high orientation to the ear is extracted with a band-pass filter, its
phase controlled with a phase shifter, and is applied to
the opposite channel to cancel crosstalk between ears.
This provides a good surround effect in the wider range
than the right and left speakers. Since the center fixing
becomes low, it is corrected with a center speaker. The
center speaker outputs reflected center sound, but its
delay time is shorter than the right and left channels.
Thus, the center fixing is assured by the Harse effect.

* Harse effect — The human ear feels that the sound source fixes in the direction of the sound that reaches it first. This is called the Harse effect.

(2) 4-channel mode

In the 4-channel mode, rear speakers are used together with three front speakers for surround reproduction in four channels. Normally, two rear speakers are used, but they output the same signal. With rear speakers, natural spreadness is felt, and each speaker reproduces indirect sound in its direction without special signal processing that is performed in the 3-channel mode.



2. Surround reproduction mode with the initial reflected sound

a. Jazz club

The initial reflected sound arrives for from 20 to 100 ms, and is attenuated in a short time. The cut-off frequency of the low-pass filter is 8 kHz, and contains comparatively many high frequency components.

b. Discotheque

The initial reflected sound is concentrated in a shorter time than the jazz club, and the delay time is set to 30 ms or less. The cut-off frequency of the low-pass filter is 6 kHz

c. Movie

The initial reflected sound ranges from 60 to 200 ms, and its level is high. The reflected sound of the center channel is reduced to increase clearness of the words. The cut-off frequency of the low-pass filter is 7 kHz, like the dolby surround.

d. Stadium

The delay time of the initial reflected sound is long, and ranges from 100 to 300 ms. It is not reflected sound, but the sound from the speaker of the PA unit is simulated as an image. The cut-off frequency of the low-pass filter is 4

3. Surround reproduction mode with reverberated sound

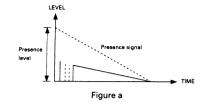
e. Arena

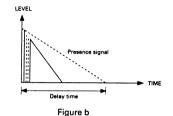
The sound field, mainly reverberated sound, is simulated with a com filter. The reverberation time is about 1.2 seconds. The cut-off frequency of the low-pass filter is 7 kHz.

f. Cathedral

Featured by very high-density reverberated sound. It is reproduced by a com filter and all-pass filter. The reverberation time is about 2.0 seconds. The cut-off frequency of the low-pass filter is 2 kHz, and the high-frequency range of the rebeverated sound is attenuate greatly.

CIRCUIT DESCRIPTION





4. Sound field parameters

Various parameters for determining the echo pattern must be controlled when surround reproduction is performed. For the KR-V9030, the user can adjust the presence level and delay time.

(1) Presence level

The level of indirect sound produced by the DSP can be varied in 2-dB steps from 0 to -20 dB. If a program source containing much indirect sound is reproduced, the indirect sound total produced by the DSP can be suppressed. (See Fig. a.)

(2) Delay time

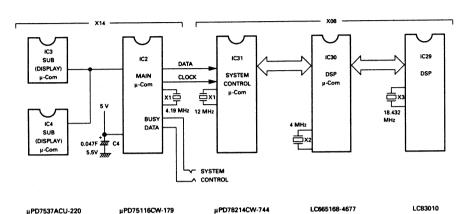
The delay time of the entire indirect sound produced by the DSP can be controlled. The delay time of the initial reflected sound that arrives first is shown on the display, and it can be varied in 5-ms steps from 5 to 100 ms.

The relationship between the positions of the sound source and listener can be controlled by changing the delay time. (See Fig. b.)

KR-V9030

CIRCUIT DESCRIPTION

Microprocessor and back-up condenser of this unit

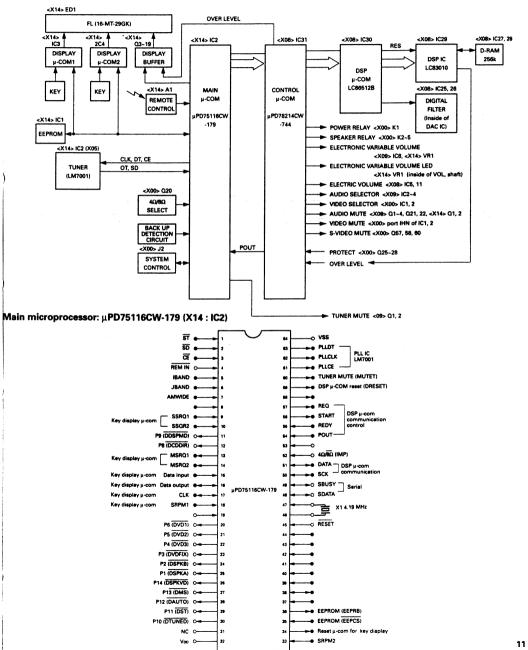


	P.C.B.		X14-	X08-				
		SUB (DISPLAY) μ-Com IC3, 4	MAIN μ-Com IC2	SYSTEM CONTROL µ-Com IC31	D.S.P μ-Com IC30			
Ī	Back-up Condenser	None	C4 0.047F 5.5V	None	None			
	Initialization (Reset)	-	Insert the AC plug to the outlet while pressing the "TUNER" key.	Pull out the AC plug from the outlet and then insert again.	Turn POWER off.			
	Operation	Insert the AC plug to the ou	utlet while pressing the selector	'CD" key				
TEST	Release	Pull out the AC plug from the	Pull out the AC plug from the outlet.					
MODE Contents (1) Turns on Power. (2) Turns on all the FL tubes. For details, refer to page 16.								

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CIRCUIT DESCRIPTION

Block diagram around microprocessor



CIRCUIT DESCRIPTION

Pin function

Pin No.	Pin name	1/0	Name	Terminal management	Description
1	P13/INT3	1	ST	PU	Stereo detection pin ACTIVE LOW (LOW = STEREO)
2	P12/INT2	ı	SD	PU	Broadcast detection pin ACTIVE LOW (LOW = with broadcast)
3	P11/INT1	1	ĈĒ	PD	Back up detection pin ACTIVE LOW
4	P10/INT0	1	REMIN	PU	Remote controller signal input pin ACTIVE LOW Normally HIGH
5	PTH03	t	IBAND	PD	Destination select pin LOW = K type HIGH = E type
6	PTH02	1	JBAND	PD	Destination select pin LOW = K or E type HIGH = J type
7	PTH01	1	AMWIDE	PD	K type AM received frequency range select LOW = 530 1700 kHz HIGH = 530 1610 kHz
8	PTH00	ı		PD, PU, Vss	No used
9, 10	TIO, TI1	1	SSRQ1, SSRQ2	PD	SSRQ signal input pin of key display microprocessor (µPD7537ACU-220 IC3, 4)
11	P23	0	P9 (DDSPMD)	PU .	FL DSP MODE (ARENA, JAZZ CLUB, etc.) display pin ACTIVE LOW (LOW = light)
12	P22/PCL	0	P8 (DCDDIR)	PU	FL "CD DIRECT" display pin ACTIVE LOW (LOW = light)
13, 14	P21/PT01 - P22/PT00	0	MSRQ1, MSRQ2	PU	·MSRQ signal output pin of key display microprocessor (µPD7537ACU-220 IC3, 4)
15	P03/SI	1	DATA INPUT	PU	EEPROM IC BA9021A DATA signal input pin of key display microprocessor (μPD7537ACU-220 IC3, 4)
16	P02/SO	1/0	DATA OUTPUT	PU	EEPROM IC BA9021A DATA signal output pin of key display microprocessor (μPD7537 ACU-220 IC3, 4)
17	P01/SCK	I/OI	CLK	PU	EEPROM IC BA9021A CLK signal output pin of key display microprocessor (μPD7537ACU-220 IC3, 4)
18	P00/INT4	ı	SRPM1	PD	SRPM signal input pin of key display microprocessor (µPD7537ACU-220 IC3, 4)
19	P123	0		PU, PD	No used
20	P122	0	P6 (DVD1)	PU (***	FL VIDEO MONITOR "1" display pin ACTIVE LOW (LOW = light)
21	P121	0	P5 (DVD2)	PU	FL VIDEO MONITOR "2" display pin ACTIVE LOW (LOW = light)
22	P120	0	P4 (DVD3)	PU	FL VIDEO MONITOR "3" display pin ACTIVE LOW (LOW = light)
23	P133	0	P3 (DVDFIX)	PU	FL "VISUAL FIX" display pin ACTIVE LOW (LOW = light)
24	P132	0	P2 (DSPKB)	PU	FL SPEAKERS "B" display pin ACTIVE LOW (LOW = light)
25	P131	0	P1 (DSPKA)	PU	FL SPEAKERS "A" display pin . ACTIVE LOW (LOW = light)
26	P130	0	P14 (DSPKVD)	PU	FL "SPEAKERS", "VIDEO MONITOR" display pin ACTIVE LOW (LOW = light)
27	P143	0	P13 (DMS)	PU	FL "ms" display pin ACTIVE LOW (LOW = light)
28	P142	0	P12 (DAUTO)	PU	FL "AUTO" display pin ACTIVE LOW (LOW = light)
29	P141	0	P11 (DST)	PU	FL "STEREO" display pin ACTIVE LOW (LOW = light)
30	P140	0	P10 (DTUNE)	PU	FL "TUNED" display pin ACTIVE LOW (LOW = light)

CIRCUIT DESCRIPTION

Pin No.	Pin name	I/O	Name	Terminal management	Description
31	NC			OP	
32	Vdd			BE	Power supply pin
33	P30	1	SRPM2	PD	SRPM signal input pin of key asplay microprocessor (µPD7537ACU-220 IC3)
34	P32	0	HRESET	PD	RESET signal output pin of key display microprocessor (μPD7537ACU-220 IC*, IC3, 4 HIGH (After reset main μ-com; for an instant) Normally LOW
35	P31	1	EEPRB	PD	EEPROM XBR9021B R/B signal input pin
36	P30	0	EEPCS	PD	EEPROM XBR9021B CS signal output pin ACTIVE LOW Normally HIGH
37~44	P43~P50	1			No used
45	RESET	1			Reset signal input pin ACTIVE LOW Normally HIGH
46	X2				System clock oscillate pin (4.19 MHz)
47	X1	1			System clock oscillate pin (4.19 MHz)
48	P63	1/0	SDATA	PD	Serial communication DATA signal I/O pin Normally Input mode
49	P62	1/0	SBUSY	PD	Serial communication BUSY signal I/O pin Normally Input mode
50	P61	VO	SYSCK	PU	SCK signal output pin for communicating to control µ-com (µPD78214CW-744) Output mode (only during communication) Normally Input mode
51	P60	I/O	SYDT	PU	DT signal output pin for communicating to control µ-com (µPD78214CW-744) Output mode (only during communication) Normally Input mode
52	P73	ı	IMP	PU, PD	4Ω /8Ω select signal input pin LOW = 8Ω HIGH = 4Ω
53	P72	1		PU, PD	No used
54	P71	I	SYPOUT	PD	PROTECTION detection signal input pin from control μ-com (μPD78214CW-744) HIGH = PROTECT ON
55	P70	1	SYREDY	PU	REDY signal input pin for communicating to control μ-com (μPD78214CW-744)
56	P83	0	SYSTRT	PU	START signal output pin for communicating to control µ-com (µPD78214CW-744) LOW output (when started communication; for an instant) LOW output Normally High output
57	P82	0	SYREQ	PU	REQ signal output pin for communicating to control μ-com (μPD78214CW-744) During communication HIGH/LOW output Normally LOW output
58	P81	0	PU	No used	
59	P80	0	DRESET	PD	Reset signal output pin of control µ-com (µPD78214CW-744). HIGH (After reset main µ-com; for an instant) Normally HIGH output
60	P93	0	MUTET	PD	MUTE signal output pin ACTIVE LOW Normally HIGH output
61	P92	0	PLLCE	PD	PLL IC LM7001 CE signal output pin Normally LOW output
62	P91	0	PLLCLK	PD	PLL IC LM7001 CL signal output pin Normally LOW output
63	P90	0	PLLDT	PD	PLL IC LM7001 DT signal output pin
64	Vss				GND pin

Terminal management: OP = Open, G = Vss, B = Vdd, BE = +5 V, PU = Pull Up, PD = Pull Down

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CIRCUIT DESCRIPTION

Initialization

Operation

Initialization takes place in the following cases:

- a. When the backup memory disappears.
- b. When the power plug is inserted into an outlet while the TUNER key is held down.

Contents

		Function	State		
	Power supply		OFF		
Amplification section	SPEAKER A		ON		
Amplification section	SPEAKER B		OFF		
			OFF		
	Muting AUDIO SELECTOR		TUNER		
			OFF		
	TAPE2 MONITOR CD DIRECT		OFF		
	LINE STRAIGHT		OFF		
			VIDEO1		
Video section	VIDEO MONITOR OUT		OFF		
	VISUAL FIX		FM		
Tuner section	BAND		Lower limit		
	FREQUENCY		AUTO		
	Tuning mode		None ·		
	Broadcast station display				
	PRESET ch. display		None		
Surround section	SURROUND		BYPASS NORMAL		
	DOLBY PRO LOGIC	CENTER MODE			
		CENTER LEVEL	-20 dB		
		REAR LEVEL	-20 dB		
		DELAY TIME	20 ms		
		TEST TONE	OFF		
	DOLBY 3 STEREO	CENTER MODE	NORMAL		
		CENTER LEVEL	-20 dB		
		TEST TONE	OFF		
	DSP	CH. MODE	4 CHANNEL		
		DSP MODE	ARENA		
		CENTER LEVEL	-20 dB		
		REAR LEVEL	-20 dB		
		DELAY TIME	60 ms		
		PRESENCE LEVEL	-8 dB		
		Each DSP MODE setting	Refer to DSP MODE initial setting.		
	INPUT LEVEL		-20 dB		
	SURROUND MEMORY conte	ents	Refer to SURROUND MEMORY initial setting.		
	SURROUND NAME display		None		

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CIRCUIT DESCRIPTION

DSP MODE initial setting

CHANNEL MODE	DSP MODE	DELAY TIME	PRESENCE LEVEL
3 CH	ARENA	60 ms	-10 dB
3 CH	JAZZ CLUB	25 ms	-6 dB
3 CH	STADIUM	70 ms	-6 dB
3 CH	DISCOTHEQUE	30 ms	-4 dB
3 CH	CATHEDRAL	70 ms	-10 dB
3 CH	MOVIE	15 ms	-16 dB
4 CH	ARENA	60 ms	-8 dB
4 CH	JAZZ CLUB	30 ms	−6 dB
4 CH	STADIUM	60 ms	-6 dB
4 CH	DISCOTHEQUE	20 ms	-4 dB
4 CH	CATHEDRAL	70 ms	−6 dB
4 CH	MOVIE	10 ms	-12 dB

SURROUND MEMORY initial setting

	M1	M2	M3	M4	M5	M6
SURROUND MODE	DSP	DSP	DSP	DSP	DSP	DSP
CHANNEL MODE	4 CH	3 СН	4 CH	3 CH	4 CH	3 CH
CENTER MODE	ARENA	ARENA	JAZZ CLUB	JAZZ CLUB	MOVIE	MOVIE
DSP MODE						
CENTER LEVEL	-18 dB	-18 dB	-17 dB	-15 dB	-17 dB	-17 dB
REAR LEVEL	-20 dB	_	-18 dB	_	-18 dB	
DELAY TIME	-40 ms	40 ms	20 ms	20 ms	10 ms	10 ms
PRESENCE LEVEL	-8 dB	-8 dB	-10 dB	-10 dB	-18 dB	-16 dB
SURROUND NAME	ARENA 1	ARENA 2	JAZZ CLUB 1	JAZZ CLUB 2	MOVIE 1	MOVIE 2

CIRCUIT DESCRIPTION

Test mode (Refer to page 10)

a. Motor-driven volume

- In the main unit test mode, the motor-driven volume is moved up or down by operating the TUNING UP/DOWN kev.
- In the main unit test mode, the motor-driven volume is stopped by operating the +10 key.
- In the main unit test mode, the TUNING UP/DOWN and +10 keys do not have their original functions.
- b. Test tone
- In the main unit test mode, if the surround mode is DOLBY PRO LOGIC or DOLBY 3 STEREO, and the BAND key is pressed, it functions as the remote controller TEST TONE ON/OFF key.
- . In the main unit test mode, if TEST TONE is on, and the CD DIRECT kev is pressed, it functions as the remote controller TEST TONE MODE key.
- . In the main unit test mode, the BAND and CD DIRECT keys do not have their original functions.
- c. Level
- . In the main unit test mode, the CENTER LEVEL key is valid and the CENTER LEVEL UP (+) key is pressed, the center level alternates among - ∞, -40, and 0 dB each time the key is pressed.
- In the main unit test mode, if the REAR LEVEL key is · valid and the REAR LEVEL UP (+) key is pressed, the rear level alternates among - ∞, -40, and 0 dB each time the key is pressed.
- In the main unit test mode, the INPUT LEVEL key is valid and the INPUT LEVEL UP (+) key is pressed, the input

level alternates among - ∞, -40, and 0 dB each time the key is pressed.

- In the main unit test mode, the PRESENCE LEVEL key is valid and the AUTO key is pressed, the input level alternates among -20, -10, and 0 dB each time the key is pressed. In the main unit test mode, the AUTO key does not have its original function.
- d. Delay time
- In the main unit test mode, if the DELAY TIME UP/DOWN key is valid and the LINE STRAIGHT key is pressed, the delay time is changed by one cycle from its minimum value to maximum value for the SURROUND
- . In the main unit test mode, the LINE STRAIGHT key does not have its original function.
- e. DSP adjustment mode [SURROUND ON/A or B 8Ω or more (on the rear panel)]

In the main unit test mode, if the DSP 4 ch ARENA (input level: 0 dB) is set, the THROUGH mode is entered, and a signal is output to the $L \rightarrow L$, C, $R \rightarrow R$, S.

300 mV/ch

Input "CD": 200 mV INPUT LEVEL: -20 dB (INPUT LEVEL DOWN) OUTPUT (X08 CN1)

- 1 PIN: R ch
- 2 PIN: GND
- 3 PIN: L ch
- 4 PIN: S (REAR) ch
- 5 PIN: C ch

Key matrix

10	1GA (13)	2GA (14)	3GA (15)	4GA (16)	5GA (17)	6GA (18)	7GA (19)	8GA (20)		41.		i.	
KR21	INPUT LEVEL	DSP	3 STE- REO	PRO LOGIC	BY PASS	SP B	SP A	POWER					
KR22	5	+ 10	TUNE +	TUNE -	BAND	CD DIRECT	LINE ST- RAIGHT	INPUT LEVEL					
KR23	7	8	9	0 –	1	2	3	4					
KR24	TAPE 1	CHARA- CTER	AUTO	DIGIT	TUNER	PHONO	CD	6					
									-	4GB (16)	3GB (15)	2GB (14)	1GB (130
									KR11	DAT	TAPE 2	DIRECT	VIDEO 1
									KR12	REAR LEVEL +	CENTER LEVEL +	VISUAL FIX	MEMO- RY
									KR13	DSPS	PSCAN	REAR LEVEL -	CENTER LEVEL -
									KR14	CENTER MODE	CHANNEL MODE	VIDEO 2	VIDEO 3

Destination setting switches

Destination	IBAND (5)*	JBAND (6)*	AM WIDE (7)*	Band	Received frequency range	Channel space	Reference frequency	
K1				FM	87.50~108.00 MHz	100 kHz	50 kHz	
NI .	_		L	AM	530~1700 kHz	10 kHz	10 kHz	
K2		L	н	FM	87.50~108.00 MHz	100 kHz	50 kHz	
N2					н	AM	530~1610 kHz	10 kHz
Ε	н			FM	87.50~108.00 MHz	50 kHz	50 kHz	
	П	L	-	AM	531~1602 kHz	9 kHz	9 kHz	
				FM	76.00~90.00 MHz	100 kHz	50 kHz	
J	-	н	-	AM	531~1602 kHz	9 kHz	9 kHz	

^{*} Means the IC2 pin No.

PLL IC LM7001 (X05:IC2)

	B01 (7)*	B03 (9)*
FM	1	0
AM	0	1
Except TUNER	0	0

	B02 (8)*
AUTO	0
MONO	1

0 : SHORT

CIRCUIT DESCRIPTION

Sub-microprocessor: μPD7537ACU-220 (X14; IC3, IC4)

Pin connection

IC4	IC3				IC3	IC4	
	RESET	\longrightarrow	$ \cdot $	42	MSRQ2	MSRQ1	
	CL1	\longrightarrow	2	41 <	SCK	CL1	
	CL2		3	40 <	so	CL2	
	VPRE	\longrightarrow	4	39>	Si		
	VLOAD	>	5	38 ←	so	FL	
KR14	KR24	\longrightarrow	6	37	P16A	P16B	
KR13	KR23	>	7	36 →	P15A	P15B	
KR12	KR22	>	8	35	P14A	P14B	
KR11	KR21	 >	9	34>	P13A	P138	
	N.C.	←	10	33	P12A	P12B	
SSRQ1	SSRQ2	←	11	32>	P11A	P11B	
SRPM1	SRPM2	←	12	31	P10A	P10B	
1GB	1GA	←	13	30>	P9A	P9B	
2GB	2GA	←	14	29>	P8A	P88	
3GB	3GA		15	28>	P7A	P7B	
4GB	4GA	←	16	27	P6A	P6B	
5GB	5GA	←	17	26	P5A	P5B	
6GB	6GA		18	25	P4A	P4B	
7GB	7GA	←—	19	24>	P3A	P3B	
8GB	8GA	←	20	23	P2A	P2B	
	Voo		21	22	P1A	P1B	
		1					

^{*} Means IC2 pin No.

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CIRCUIT DESCRIPTION

Pin function

Pin No.	Pin name	VO.	IC 3	IC 4	Description
1	RESET	1	RESET		Display microprocessor reset pin. Controlled by main microprocessor. Reset by HRESET (B4 PIN) of µPD75116CW-179.
2	CL1	ı			System clock oscillate pin of display microprocessor (600 kHz).
3	CL2	0			
4	VPRE				Power supply pin of predriver.
5	VLOAD				Negative power supply pin (-30 V).
6	P53	ī	KR24	KR14	Return signal input pin of key matrix.
7	P52		KR23	KR13	
8	P51		KR22	KR12	1
9	P50		KR21	KR11	
10	P23	0	N. C.		No used (open)
11	P22	0	SSRQ2	SSRQ1	Demand signal output pin for communicating from display microprocessor to main microprocessor. Normally - Low. When demanded to communication (when key is pressed) - High
12	P21	0	SRPM2	SRPM1	Permission signal input pin for communicating from display microprocessor to main microprocessor.
13 20	P103 P110	0,	8GA 1GA	8GB 1GB	FL grid control signal. Controls 1GA (1GB) to 8GA (8GB) of fL.
21	Vpp	 	104	+	Power supply pin (+5 V)
22 37	P93 P30	0.	P1A P16A	P1B P16B	FL segment control signal
38	P03/SI	1	SI		Input signal pin for communicating to main micro- processor. (Display data input)
39	P02/SO	0	so		Output signal pin for communicating to main micro- processor. (Key data output)
40	P01/SCK	1	SCK		Clock signal pin for communicating to main micro- processor.
41	P00/INT0	ı	MSRQ2	MSRQ1	Demand signal pin for communicationg from main microprocessor.
42	Vss				GND pin

^{*:} P ch open drain With mask option resistor

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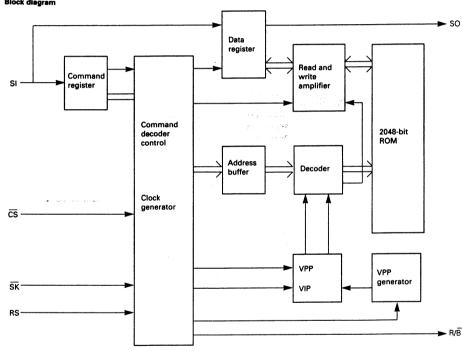
CIRCUIT DESCRIPTION

2K serial EEPROM: XRM9021A (X14: IC1)

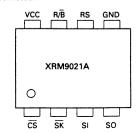
Features

- 128-word × 16-bit 2 K serial EEPROM
- Single power supply
- Serial data input/output
- Automatic erasing function for writing data
- Small package with DIP 8 pins
- Input/output is TTL compatible.
- High reliable fine CMOS process

Block diagram



Pin connection

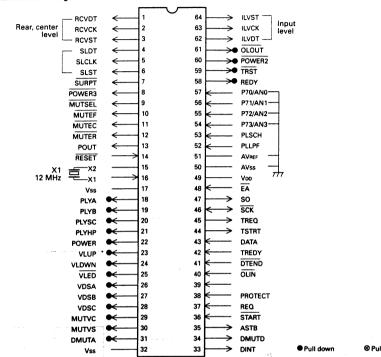


Pin No.	Pin name	I/O	Description
1	cs	Input	Chip select input
2	SK	Input	Serial data clock input
3	SI	Input	Operating code, address and serial data input
4	SO	Output	Serial data output
5	GND	-	Gnd
6	RS	Input	Rest signal input
7	R/B	Output	READY, BUSY status signal output
8	VCC	-	Connect the power supply (5 V ± 10%)

CIRCUIT DESCRIPTION

System control microprocessor: µPD78214CW-744 (X08: IC31)

Terminal connection diagram



Pin function

Pin No.	Pin name	1/0	Name	Description		
1	P03	0	RCVDT	Rear,center electric volume		
2	P04	0	RCVCK	TC9213P control pin CK signal		
3	P05	0	RCVST	STB signal		
4	P06	0	SLDT	Switch array IC DATA signal		
5	P07	0	SLCLK	TC9162N, 9163N CK signal		
6	P67	0	SLST	TC9164N control pin ST signal		
7	P66	0	SURPT	Surround (DSP IC) oscillate control H: Stop L: Oscillate		
8	P65	0	POWER3	Port used to synchronize with the timing of the power up of the D/A converter		
9	P64	0	MUTSEL	Selector mute pin H: MUTE OFF L: MUTE ON		
10	P63	0	MUTEF	Front signal mute pin H: MUTE OFF L: MUTE ON		
11	P62	0	MUTEC	Center signal mute pin H: MUTE OFF L: MUTE ON		
12	P61	0	MUTER	Rear signal mute pin H: MUTE OFF L: MUTE ON		
13	P60	0	POUT	Pin that notifies the main μ-com of protection detection		
14	RESET	ı		Reset pin		

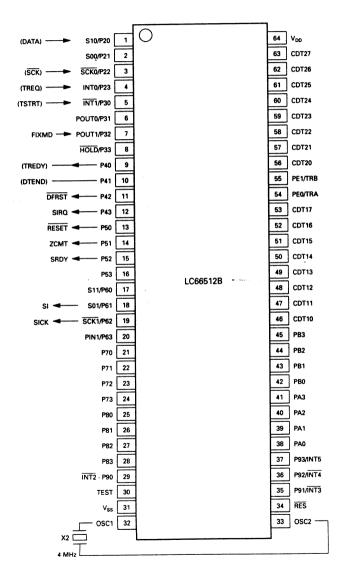
CIRCUIT DESCRIPTION

Pin No.	Pin name	1/0	Name	Description
15	X2			System clock oscillator connect pin
16	X1	1		
17	Vss	0		Gnd
18	P57	0	RLYA	Speaker A relay control pin L: Power OFF H: Power ON
19	P56	0	RLYB	Speaker B relay control pin L: Power OFF H: Power ON
20	P55	0	RLYSC	Speaker (SURROUND CENTER) relay control pin L: Power OFF H: Power ON
21	P54	0	RLYHP	Headphone relay control pin L: Power OFF H: Power ON
22	P53	0	POWER	Powersupply control pin L: POWER OFF H: POWER ON
23	P52	0	VLUP	Master volume UP control pin
24	P51	0	VLDWN	Master volume DOWN control pin
25	P50	0	VLED	Master volume LED control pin L: LED ON H: LED OFF
26	P47	0	VDSA	Video selection control pin
27	P46	0	VDSB	VDSB 1 0 1
28	P45	0	VDSC	VDSC 1 1 0 V1 - V3
29	P44	0	MUTVC	Composie video mute control pin SOUT - V2 V3 VIDEO 3
30	P43	0	MUTVS	S ch video mute control pin MUTE is OFF only entered the VIDEO 3 mode
31	P42	0	DMUTA	DSP analog mute control pin SURROUND ON: MUTE OFF SURROUND OFF: MUTE ON When switched : MUTE ON
32	Vss			Gnd
33	P41	0	DINT	No used
34	P40	0	DMUTD	DSP digital mute control pin
35	ASTB			No used
36	P20/NMI	1	START	START signal input pin for communicating to main μ-com
37	P21	1	REQ	REQ signal input pin for communicating to main μ-com
38	P22	1	PROTECT	Protection signal detection pin
39	P23			No used
40	P24	1	OLIN	Over level signal detection pin
41	P25	1	DTEND	DTEND signal input pin for communicating to DSP IC control µ-com
42	P26	-	TREDY	TREDY signal input pin for communicating to DSP IC control μ-com
43	P27/SI	1	DATA	DATA signal input SI port of communicating to mainl μ-com
44	P30	0	TSTRT	START signal output pin for communicating to DSP IC control μ-com
45	P31	0	TREQ	REQ signal output pin for communicating to DSP IC control μ-com
46	P32/SCK	1/0	SCK	SCK I/O pin for communicating to main μ-com and DSP IC control μ-com
47	P33/SO	0	so	SO signal output pin for communicating to DSP IC control μ-com
48	EA			No used
49	Voo			Power supply pin
50,51	AVss, Avref			No used
52 ~57	P75 ~ P70	1		No used
58	P34	0	REDY	REDY signal output pin for communicating to main μ-com
59	P35	0	TRST	DSP IC control μ-com reset pin
60	P36	0	POWER2	Port used to synchronize with the timing of the power up of the D/A converter
61	P37	0	OLOUT	Over level output pin L: FL light H: FL not light
62	P00	0	ILVDT	Input level electric volume DATA signal
63	P01	0	ILVCK	CK signal
64	P02	0	ILVST	ST signal

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CIRCUIT DESCRIPTION

DSP μ-Com: LC66516B-4677 (X08: IC30)



KR-V9030

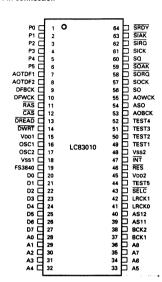
CIRCUIT DESCRIPTION

Pin No.	Pin name	1/0	Name	Description
1	SI0/P20	1	DATA	DATA signal input pin from system control μ-com
2	SO0			No used
3	SCK/P22	1	SCK	Clock signal input pin from system control μ-com
4	INTO/P23	1	TREQ	TREQ signal input pin from system control μ-com
5	INT1/P30	1	TSTRT	TSTRT signal input pin from system control µ-com
6	Pout0/P31	T		No used
7	Pout1/P32	1	FIXMD	Fixation terminal mode setting pin. Low: Normal mode High: Fixation terminal mode
8	HOLD/P33	1	TSTRT	HOLD mode control input
9	P40	0	TREDY	TREDY signal output pin to system control μ-com
10	P41	0	DTEND	At mode change (command 0 \sim 2) and during clear the DRAM, transfer the data to DSP IC.
11	P42	0	DFRST	Digital filter reset signal output pin (Normally High)
12	P43	0	SIRQ	DSP IC LC83010 SIRQ signal output pin
13	P50	0.	RES	DSP IC LC83010 Reset signal output pin (Normally High)
14	P51	0	ZCMT	Zero cross mute control signal output pin
15	P52	0	SRDY	DSP IC LC83010 SRDY signal output pin
16,17	P53, SU/P06			No used was a second
18	SO1/P61	0	SI	DSP IC LC83010 SI signal output pin
19	SCK1/P62	0	SICK	DSP IC LC83010 SICK signal output pin
20 ~ 28	PIN1/P63 P70 ~ P73 P80 ~ P83	0		No used
29	INT2/P90			DSP IC LC83010 SIAK signal input pin
30	TEST			CPU test pin. Connected to Vss.
31	Vss			GND pin
32	OSC1	1		System clock oscillator pin
33	OSC2	0		System clock oscillator pin
34	RES	ı		System reset signal input pin
35 ~ 37	P91 ~93 INT3 ~ INT 5			No used
38 ~ 45	PA0 ~ PA3 PB0 ~ PB3	1		No used
46 ~ 53	PC0	T	CDT10 ~ 17	Correspond to bit 0 ~ 7 of data address 1 of command data in the fixed pin mode.
54	PE0/TRA	1		Correspond to 2 low-order bits of command data in the fixed pin mode. The fixed pin
55	PE1/TRB	1		mode can be set to 00, 01,02 or 03.
56 ~ 63	P35	ı	CDT20 ~ 27	Corresponds to bit 0 ~ 7 of data address 2 of command data in the fixed pin mode.
64	Vpp			Power supply

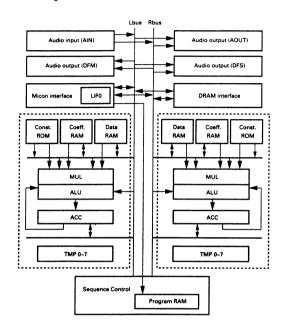
CIRCUIT DESCRIPTION

DSP IC: LC83010 (X08: IC29)

Pin connection



Block diagram



CIRCUIT DESCRIPTION

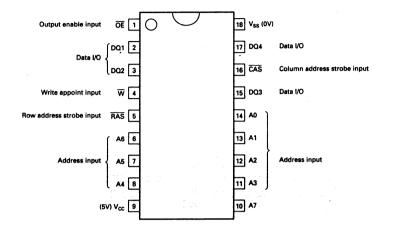
Pin No.	Pin name	1/0	Description				
1	P0	1	Digital mute - High: mute; Low: unmute during DSP program				
2	P1	1	Soft muting - High during DSP program: Soft mute with time constan	it of 1 ms; Low: Unmute			
3	P2	0	Overflow detection If the input data from the A/D converter becomes the maximum positive or negative value a low signal is output, held for 100 ms, and goes high.				
4	P3	1	Phase shifter control The phase shifter is turned on and off during 3 program. Low: on; High; off. Always used with "LOW".	Phase shifter control The phase shifter is turned on and off during 3-channel sound field			
5	P4		Direct sound add control Control whether direct sound is added in the program. High: Add; Low: Do not add. Always used with "LOW".	ne DSP during sound fied			
6	P5	1/0	General input/output port No used (open)				
7	AOTDF2	0	Audio data output 1 C ch and S ch data is output during Dolby pro lo 3 stereo and 3-CH are set, only C ch data is output.	gic and 4-ch sound field. If			
8	AOTDF2	0	Audio data output 2 Decoded L/R data is output for Dolby. The L/R s for sound field.	ound field signal is output			
9	DFBCK	0	Bit clock for AOTDF 1 and 2 48 fs bit clock is output.				
10	DFWCK	0	Word clock for AOTDF 1 and 2 No used	*			
11	RAS	0	For row address strobe DRAM access control				
12	CAS	0	For column address strobe DRAM access control				
13	DREAD	0	DRAM read control signal				
14	DWRT	0	DRAM write control signal				
15, 45	VDD1, 2	1	Power supply pin				
18, 48	VSS1, 2	†	GND pin				
16	OSC1		Crystal oscillator pin				
17	OSC2	0	Crystal oscillator pin	·			
19	FS3840	0	384fs output pin				
20 ~ 27	D0 ~ D7	1/0	DRAM data I/O pin				
28 ~ 36	A0 ~ A8	0	DRAM address output pin (A8 is no used)				
37	BCK1	1	No used				
38	BCK2	0	Bit clock output pin 32fs bit clock output for A/D				
39	ASI1	- 1	No used				
40	ASI2	-	Audio data input pin 2 Data input from A/D				
41	LRCKO	0	L/R clock output pin				
42	LRCKI	1	No used				
43	SELC	1	Self oscillation and external clock input switching				
44	TEST 5	0	Test pin Used by open				
46	RES	1	Reset pin				
47	INT	_	No used				
49 ~ 52	TEST 1 ~ 4	1	Test pin Connected to GND				
53	AOBCK	0	No used				
54	ASO	0	Audio data output (overflow detection) Used by the the KR-V9030 to o	detect overflow for Dollar			
55 ~ 59	A0WCK etc.		No used	dotted evernow for boildy.			
60	SI	1	Serial data input from µ-com				
61	SICK	1	Serial clock input of SI input				
62	SIRQ	1	DSP ↔ µ-com interface				
63	SIAK	0	Output signal to indicate that the SI serial communication is executing	пленасе			
64	SRDY		Input signal to indicate that the mail box communication is finished				

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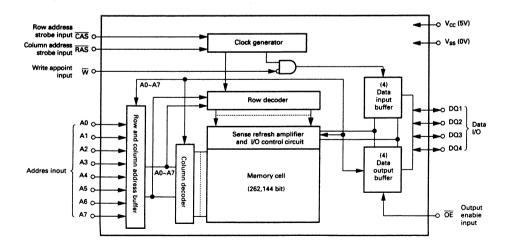
CIRCUIT DESCRIPTION

D-RAM IC: LM33464G-12 (X08: IC27, 28)

Pin connection



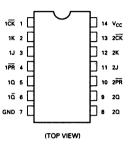
Block Diagram



CIRCUIT DESCRIPTION

Dual J-K flip flop with preset: TC74HC113AP (X08: IC18)

Pin connection

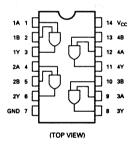


	INP	UTS		оит	PUTS	FUNCTION
PR	J	К	CK	Q	ā	
L	×	х	х	н	L	PRESET
н	L	L	J	Qn	Qπ	NO CHANGE
н	L	н	J	L	н	-
н	н	L	J	н	L	_
н	н	н	J.	Ω̈́n	Qn	TOGGLE
н	х	х	1	Qn	Qn	NO CHANGE

X : Don't care

Quad 2-input AND gate: TC74HC08AP (X08: JC19)

Pin connection



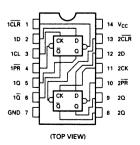
Truth	

Truth table

A	В	Y
L	L	L
L	н	L
н	L	L
н	н	н

Dual D-type flip flop with preset and clear: TC74HC74AP (X08:IC20, 21) Truth table

Pin connection



	INP	UTS		OUT	PUTS	FUNCTION
CLR	PR	D	СК	α	·ā	
L	н	х	x	L	н	CLEAR
Н	L	х	х	Н	L	PRESET
L	L	×	×	н	H	_
н	н	L	1	L	Η	_
н	Ĥ	н	1	н	L	-
Н	н	x	J.	Qn	Qn	NO CHANGE

X : Don't care

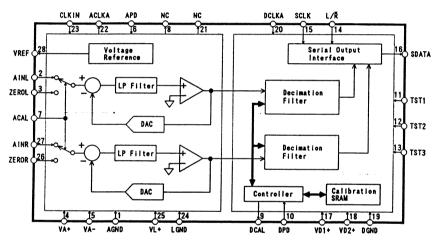
CIRCUIT DESCRIPTION

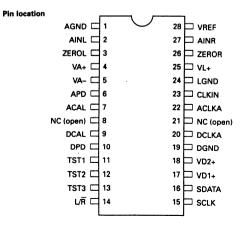
A/D converter IC: CS5326-KP (X08: IC 17)

Features

- 18-bit stereo A/D conversion system Simultaneous two-channel sampling Loopback noise prevention digital filter built in Sample and hold circuit, reference voltage source built in
- 64 time over sampling method
 Support digital audio system sampling rates of 32, 44.1, and 48 kHz
- Excellent dynamic characteristics in all bands S/(N+D): 95 dB Dynamic range: 96 dB
- Linear phase digital filter
 Pass band: 0 to (22/48) fs
 Pass band ripple: 0.001 dB
 Suppression range attenuation: 86 dB

Block diagram





CIRCUIT DESCRIPTION

Pin No.	Pin name	1/0	Description			
1	AGND	T -	Analog GND pin			
2	AINL		L ch analog input pin. The full scale input level is ±3.68 V. It is recommended that a capacitor of 10 nF or more should be connected between this pin and AGND.			
3	ZEROL	1	L chizero level input pin Normally, the input voltage at this pin is the zero level, and the le channel offset is calibrated.			
4	VA+	-	Analog positive power supply (+5 V)			
5	VA-		Analog negative power supply (-5 V)			
6	APD	1	Analog power down pin When this pin is high, the power down mode is entered. Normally, connected to the DPD pin. This pin can be used to synchronize several CS5328 samplings with the DPD pin.			
7	ACAL		Analog calibration pin Normally, connected to the DCAL pin. When this pin is high, the L/R input channel is connected to the zero-level input pin (ZEROL, ZEROR). When low, connected to the analog input pin (AINL, AINR).			
8	NC	-	No used (Open)			
9	DCAL	0	Digital calibration pin Normally, used as an input signal for the ACAL pin. When a power down signal is input to the DPD pin, it rises immediately, and after the 4096L/R period (about 85 ms for 6.144 MHz) after the DPD pin falls, goes low, indicating the end of offset calibration. If system calibration is performed, the channel select signal for the external MUX can be used.			
10	DPD		Digital power down pin When this pin is high, the power down mode is entered. After the power is switched on, input a positive pulse to this pin at least once to perform calibration.			
11	TST1	1	Test pin			
12	TST2	I				
13	TST3	- 1				
14	L/Ř	1	Input channel select pin Selects data channel output from the SDATA pin. If high, L channel data is output, and if low, R channel data is output. The master clock divided by 128 is input.			
15	SCLK	-	Serial data output clock pin Output data changes by one bit when the clock rises. Normally, the master clock divided by two is input.			
16	SDATA	0	Serial data output pin Data is output as a complement of 2's from MSB in order. When SCLK rises, one bit of data is output. A low signal is output if 19 SCLK or more are input.			
17	VD1+	-	Digital positive power supply (+5 V)			
18	VD2+	-				
19	DGND	-	Digital GND pin			
20	DCLKA	0	Digital system clock pin Connect to the DCLKA pin. The master clock divided by two is input.			
21	NC	-	No used (Open)			
22	ACLKA	0	Analog system clock pin Connect the DCLKA pin. The master clock divided by two is output.			
23	CLKIN	1	Master clock pin The clock divided by two is in the sampling rate for the delta sigma modulator If the clock is 6.144 MHz, the output word rate per channel is 48 kHz.			
24	LGND	-	Digital GND pin			
25	VL+	-	Power supply for digital circuit (+5 V)			
26	ZEROR	1	R channel zero-level input pin Normally, using the input voltage at this pin as zero level, the right channel offset is calibrated. Normally, connected to the GND pin.			
27	AINR	1	R channel analog input pin The full scale input level is ±3.68 V. It is recommended that a capacitor of 10 nF or more should be connected between this pin and AGND.			
28	VREF	0	Reference power source (–3.68 V) Normally, a 6.8 μ F electrolytic capacitor and 0.1 μ F ceramic capacitor are connected in parallel between this pin and AGND.			

KR-V9030

CIRCUIT DESCRIPTION

16 bit D/A converter for audio circuit(8 fs, with digital filter): LC7883K (X08: IC25, 26)

Pin connection

		· —	•
CH1 OUT	1	28	CH2 OUT
Vref H	2	27	VREF L
AVDD	3	26	AGND
DVDD	4	25	XOUT
BCLK	5	24	XIN
DATA	6	23	CLK OUT
LRCK	7	22	DGND
TEST	8	21	TEST
ATT	9	20	TEST
SHIFT	10	19	MODE
LATCH	11	18	SOC1
INIT B	12	17	SOC2
TEST	13	16	D/N
EMPH2	14	15	EMPH1
	l .		ı

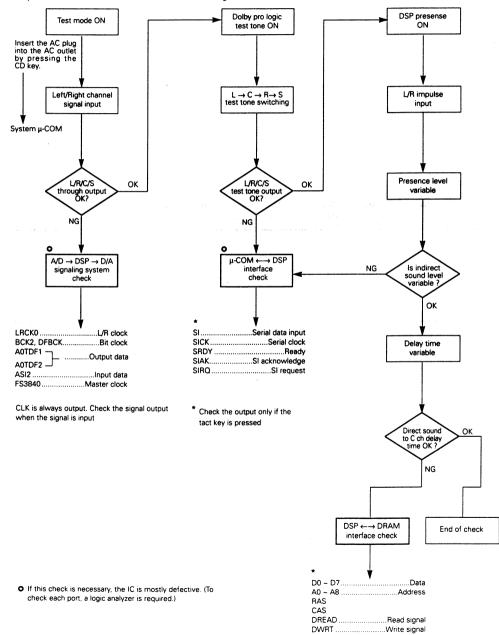
Pin function

Pin No.	Pin name	1/0	Description
1	CH1OUT	0	DAC CH-1 output pin
2	Vref H	R	Reference voltage "H" input pin
3	AVDD	Р	Power supply pin of analog
4	DVDD	Р	Power supply pin of digital
5	BCLK	T	Bit clock pin
6	DATA	1	Digital audio data input pin Serial bit data is input from MSB to LSB.
7	LRCK	1	LR clock input pin LRCK = "H" CH 1 LRCK = "L" CH 2
8	TEST	1	Test pin (Normally "L")
9	ATT	1	Attenuation data input pin Serial bit data is input from LSB to MSB.
10	SHIFT	1	Attenuation data transfer clock input pin
11	LATCH	1	Attenuation data transfer latch clock input pin
12	INITB	1	Initialize signal input pin (Normally "L")
13	TEST	1	Test pin (Normally *L*)
14	EMPH2	1	Deemphasis setting pin
15	EMPH1	1	
16	D/N	- 1	Double speed/ Normal speed select pin
17	SOC2	1	Input source select pin
18	SOC1	1	
19	MODE	1	Active mode setting pin
20	TEST	1	Test pin (Normally "L")
21	TEST	I	
22	DGND	P	GND pin of digital
23	CLKOUT	0	Clock output pin 392Fs: 1/2 XOUT 384Fs, 448Fs, 512Fs: 1/4 XOUT
24	XIN	1	Crystal oscillator input pin
25	XOUT	1	Crystal oscillator output pin
26	AGND	P	GND pin of analog
27	VrefL	R	Reference voltage "L" input pin
28	CH2OUT	1	DAC CH-2 output pin

TROUBLESHOOTING

DSP circuit troubleshooting

DSP μ-COM ←→ DSP ←→ D-RAM Troubleshooting

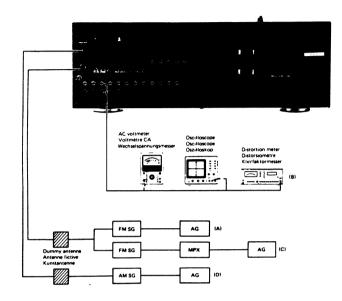


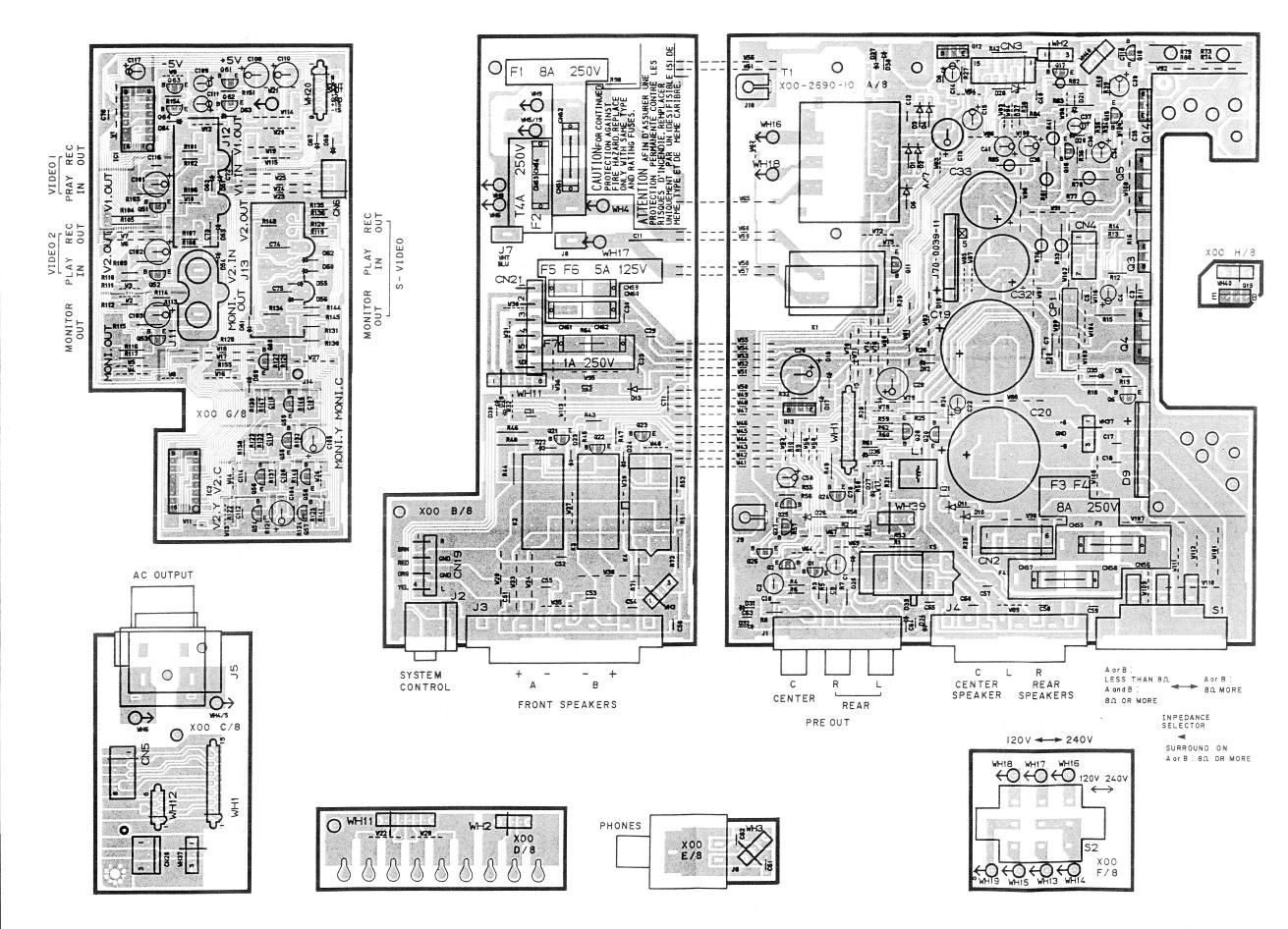
ADJUSTMENT

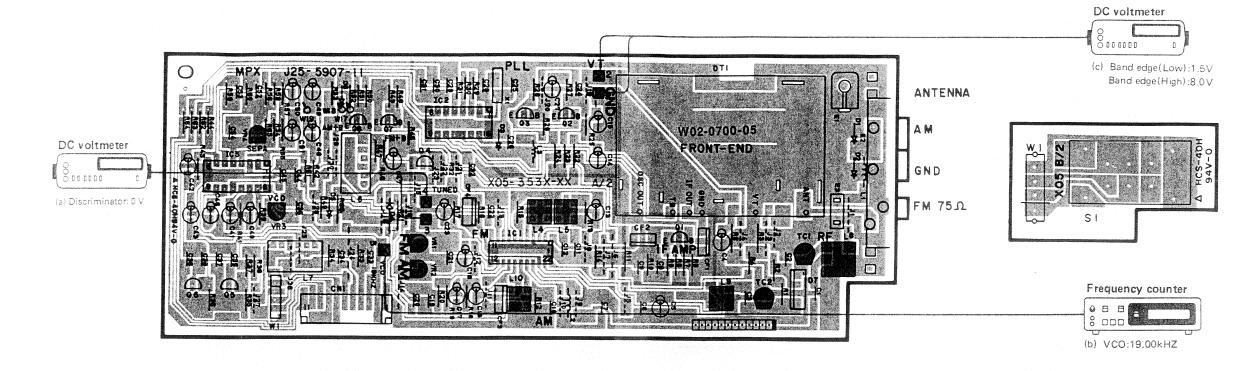
ADJUSTMENT

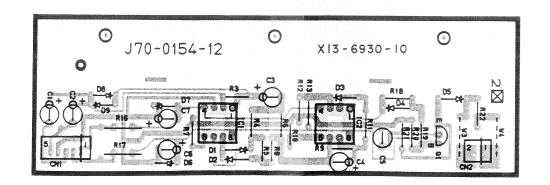
	11 1101.1	eplace the front end	OUTPUT	TUNER	ALIGNMENT		
						AL LON DOD	١
No.	ITEM	SETTINGS	SETTINGS	SETTINGS	POINTS	ALIGN FOR	FI
F M	SECTION		LECTOR: FW	r			_
		(A)	Connect a DC		١ ا		1
		98.0MHz	voltmeter between	AUTO	L4		١,
1	DISCRIMINATOR	1kHz,±75kHz dev	TP3 and TP4.	or MONO	(X05-)·	0.0	(1
		60dBµ(ANT input)	(X05-)	98.0MHz			L
		(C)		į			
		98.0MHz		1			
2	DISTORTION	1kHz, ±68.25MHz dev				L5	l
	(MONO)	Selector:L or R	· (B)	98.0MHz	(X05-)	Minimum distortion	l
		Pilot:±6.75kHz dev		1			
		60dBµ(ANT input)		ļ			┡
		(A)	Connect a frequency		l		
_		98.0MHz	counter between	AUTO	VR3		١.
3	VCO	0 dev	TP5 and GND.	98.0MHz	(X05-)	19.00kHz	(1
		100dBμ(ANT input)	(X05-)				L
		(C)			1		1
		98.0MHz					1
4	DISTORTION	1kHz,±68.25kHz dev			IFT		
	(STEREO)	Selector:L or R	(B)	98.0MHz	(¥02-)	Minimum distortion. (L or R)	
		Pilot:±6.75kHz dev					1
		60dBμ(AMT input)					L
		(c)			1 1		l
5	SEPARATION	98.0MHz	*	AUTO	VR4	Minimum crosstalk	ı
		Stereo signal	(B) 1 1	98.0MHz	(X05-)		
		60dB(ANT input)					L
. 1		(A)	•				l
6	TUNING LEVEL	98.0MHz	4-1	AUTO	VR1	Adjust VR1	l
		0dev	(B)	or MONO	(X05-)	and stop at the point	1
	l1	14dBμ(ANT input) 75Ω		98.0MHz		where ED1(TUNED)goes on.	L
A M	SECTION	I (X05-) SE	LECTOR: AN	·	, , ,		_
			Connect a DC	İ	١ ا		١.
1)	BAND EDGE	-	voltmeter between	-	L9	1.5V	(0
	(Low)		TP1(GND) and TP2.		(X05-)		<u> </u>
			Connect a DC	4.1			
2)	BAND EDGE	-	voltmeter between	-	TC2	8. OY	(4
	(High)		TP1(GND) and TP2.	L	(X05-)		<u> </u>
			Repeat alignments (1)	and (2) sever	al times.		
		(D)				Maximum amplitude and	
• •			/n\		1 1		
3)	RP ALIGNMENT	600kHz	(B)	-	L8	symmetry of the oscilloscope	
3)	RF ALIGNMENT (1)	600kHz 20dBµ(ANT input)	(B)	-	L8 (X05-)	display.	
	(1)	600kHz 20dB#(ANT input) (D)		-	(X05-)	display. Naximum amplitude and	
	(1) RF ALIGNMENT	600kHz 20dBµ(ANT input) (D) 1400kHz	(B)	-	(X05-) TC1	display. Maximum amplitude and symmetry of the oscilloscope	
	(1)	600kHz 20dB#(ANT input) (D)	(B)	-	(X05-) TC1 (X05-)	display. Naximum amplitude and	
	(1) RF ALIGNMENT	600kHz 20dB#(ANT input) (D) 1400kHz 20dB#(ANT input)		- and (4) sever	(X05-) TC1 (X05-)	display. Maximum amplitude and symmetry of the oscilloscope display.	
4)	(1) RF ALIGNMENT (2)	800kHz 20dB#(ANT input) (D) 1400kHż 20dB#(ANT input)	(B) Repeat alignments (3)	and (4) sever	TC1 (X05-)	display. Maximum amplitude and symmetry of the oscilloscope display. Maximum amplitude and	
4)	(1) RF ALIGNMENT	800kHz 20dBµ(ANT input) (D) 1400kHż 20dBµ(ANT input) (D) 1000kHz	(B)	and (4) sever	(X05-) TC1 (X05-) Tal times.	display. Maximum amplitude and symmetry of the oscilloscope display. Maximum amplitude and symmetry of the oscilloscope	
4)	(1) RF ALIGNMENT (2)	600kHz 20dBµ(ANT input) (D) 1400kHz 20dBµ(ANT input) (D) 1000kHz 20dBµ(ANT input)	(B) Repeat alignments (3)	and (4) sever	TC1 (X05-)	display. Maximum amplitude and symmetry of the oscilloscope display. Maximum amplitude and symmetry of the oscilloscope display.	
4) 5)	(1) RF ALIGNMENT (2) IF TRANSFORMEN	800kHz 20dBµ(ANT input) (D) 1400kHż 20dBµ(ANT input) (D) 1000kHz 20dBµ(ANT input)	(B) Repeat alignments (3)	- and (4) sever	(X05-) TC1 (X05-) ral times. L10 (X05-)	display. Maxisus amplitude and symmetry of the oscilloscope display. Maxisus amplitude and symmetry of the oscilloscope display. Adjust VR2	
4) 5)	(1) RF ALIGNMENT (2) IF TRANSFORMEN	800kHz 20dB#(ANT input) (D) 1400kHż 20dB#(ANT input) (D) 1000kHz - 20dB#(ANT input) (D) 1000(999)kHz	(B) Repeat alignments (3)	- and (4) sever	(X05-) TC1 (X05-) al times. L10 (X05-) VR2	display. Maximum amplitude and symmetry of the oscilloscope display. Maximum amplitude and symmetry of the oscilloscope display. Adjust VR2 and stop at the point	
5)	(1) RF ALIGNMENT (2) IF TRANSFORMEN TUNING LEYEL	600kHz 20dBw(ANT input) (D) 1400kHz 20dBw(ANT input) (D) 1000kHz - 20dBw(ANT input) (D) 1000(999)kHz 36dBw(ANT input)	(B) Repeat alignments (3)	and (4) sever	(X05-) TC1 (X05-) ral times. L10 (X05-)	display. Maxisus amplitude and symmetry of the oscilloscope display. Maxisus amplitude and symmetry of the oscilloscope display. Adjust VR2	
4) 5)	(1) RF ALIGNMENT (2) IF TRANSFORMEN	600kHz 20dBw(ANT input) (D) 1400kHz 20dBw(ANT input) (D) 1000kHz - 20dBw(ANT input) (D) 1000(999)kHz 36dBw(ANT input)	(B) Repeat alignments (3) (B)	- and (4) seven	(X05-) TC1 (X05-) al times. L10 (X05-) VR2	display. Maximum amplitude and symmetry of the oscilloscope display. Maximum amplitude and symmetry of the oscilloscope display. Adjust VR2 and stop at the point	
4) 5)	(1) RF ALIGNMENT (2) IF TRANSFORMEN TUNING LEYEL	600kHz 20dBw(ANT input) (D) 1400kHz 20dBw(ANT input) (D) 1000kHz - 20dBw(ANT input) (D) 1000(999)kHz 36dBw(ANT input)	(B) Repeat alignments (3) (B) (B)	- and (4) sever	(X05-) TC1 (X05-) *al times. L10 (X05-) VR2 (X05-)	display. Maximum amplitude and symmetry of the oscilloscope display. Maximum amplitude and symmetry of the oscilloscope display. Adjust VR2 and stop at the point	
4) 5)	(1) RF ALIGNMENT (2) IF TRANSFORMER TUNING LEVEL DIO SECT	600kHz 20dBw(ANT input) (D) 1400kHz 20dBw(ANT input) (D) 1000kHz - 20dBw(ANT input) (D) 1000(999)kHz 36dBw(ANT input)	(B) Repeat alignments (3) (B) (B) (E) Connect a DC voltmeter	-	(X05-) TC1 (X05-) ral times. L10 (X05-) VR2 (X05-) VR1(L)	display. Maximum amplitude and symmetry of the oscilloscope display. Maximum amplitude and symmetry of the oscilloscope display. Adjust VR2 and stop at the point where EDI(TUNED) goes on.	
4) 5)	(1) RF ALIGNMENT (2) IF TRANSFORMER TUNING LEVEL DIO SECT	600kHz 20dBw(ANT input) (D) 1400kHz 20dBw(ANT input) (D) 1000kHz - 20dBw(ANT input) (D) 1000(999)kHz 36dBw(ANT input)	(B) Repeat alignments (3) (B) (B) (Connect a DC voltmeter across TP7 and TP8	and (4) sever	(X05-) TC1 (X05-) -al times. L10 (X05-) VR2 (X05-) VR2 (X05-)	display. Maximum amplitude and symmetry of the oscilloscope display. Maximum amplitude and symmetry of the oscilloscope display. Adjust VR2 and stop at the point	(
4) 5)	(1) RF ALIGNMENT (2) IF TRANSFORMER TUNING LEVEL DIO SECT	600kHz 20dBw(ANT input) (D) 1400kHz 20dBw(ANT input) (D) 1000kHz - 20dBw(ANT input) (D) 1000(999)kHz 36dBw(ANT input)	(B) Repeat alignments (3) (B) (B) (Connect a DC voltmeter across TP7 and TP8 (CP1:L)	-	(X05-) TC1 (X05-) -al times. L10 (X05-) VR2 (X05-) VR1(L) VR2(R) VR3(C)	display. Maximum amplitude and symmetry of the oscilloscope display. Maximum amplitude and symmetry of the oscilloscope display. Adjust VR2 and stop at the point where EDI(TUNED) goes on.	(4
4) 5)	(1) RF ALIGNMENT (2) IF TRANSFORMER TUNING LEVEL DIO SECT	600kHz 20dBw(ANT input) (D) 1400kHz 20dBw(ANT input) (D) 1000kHz - 20dBw(ANT input) (D) 1000(999)kHz 36dBw(ANT input)	(B) Repeat alignments (3) (B) (B) (Connect a DC voltmeter across TP7 and TP8 (CP1:L) TP5 and TP6	-	(X05-) TC1 (X05-) -al times. L10 (X05-) VR2 (X05-) VR2 (X05-)	display. Maximum amplitude and symmetry of the oscilloscope display. Maximum amplitude and symmetry of the oscilloscope display. Adjust VR2 and stop at the point where EDI(TUNED) goes on.	((
4) 5)	(1) RF ALIGNMENT (2) IF TRANSFORMER TUNING LEVEL DIO SECT	600kHz 20dBw(ANT input) (D) 1400kHz 20dBw(ANT input) (D) 1000kHz - 20dBw(ANT input) (D) 1000(999)kHz 36dBw(ANT input)	(B) Repeat alignments (3) (B) (B) (Connect a DC voltaeter across TP7 and TP8 (CP1:L) TP5 and TP6 (CP2:R)	-	(X05-) TC1 (X05-) -al times. L10 (X05-) VR2 (X05-) VR1(L) VR2(R) VR3(C)	display. Maximum amplitude and symmetry of the oscilloscope display. Maximum amplitude and symmetry of the oscilloscope display. Adjust VR2 and stop at the point where EDI(TUNED) goes on.	(4
4) 5) 6)	(1) RF ALIGNMENT (2) IF TRANSFORMER TUNING LEVEL DIO SECT	600kHz 20dBw(ANT input) (D) 1400kHz 20dBw(ANT input) (D) 1000kHz - 20dBw(ANT input) (D) 1000(999)kHz 36dBw(ANT input)	(B) Repeat alignments (3) (B) (B) (C) Connect a DC voltmeter across TP7 and TP8 (CP1:L) TP5 and TP6 (CP2:R) Ipin and 2pin	-	(X05-) TC1 (X05-) -al times. L10 (X05-) VR2 (X05-) VR1(L) VR2(R) VR3(C)	display. Maximum amplitude and symmetry of the oscilloscope display. Maximum amplitude and symmetry of the oscilloscope display. Adjust VR2 and stop at the point where EDI(TUNED) goes on.	((
3) 5) 6) AU 1>	(1) RF ALIGNMENT (2) IF TRANSFORMER TUNING LEVEL DIO SECT	600kHz 20dBw(ANT input) (D) 1400kHz 20dBw(ANT input) (D) 1000kHz - 20dBw(ANT input) (D) 1000(999)kHz 36dBw(ANT input)	(B) Repeat alignments (3) (B) (B) (Connect a DC voltaeter across TP7 and TP8 (CP1:L) TP5 and TP6 (CP2:R)	-	(X05-) TC1 (X05-) -al times. L10 (X05-) VR2 (X05-) VR1(L) VR2(R) VR3(C)	display. Maximum amplitude and symmetry of the oscilloscope display. Maximum amplitude and symmetry of the oscilloscope display. Adjust VR2 and stop at the point where EDI(TUNED) goes on.	(4

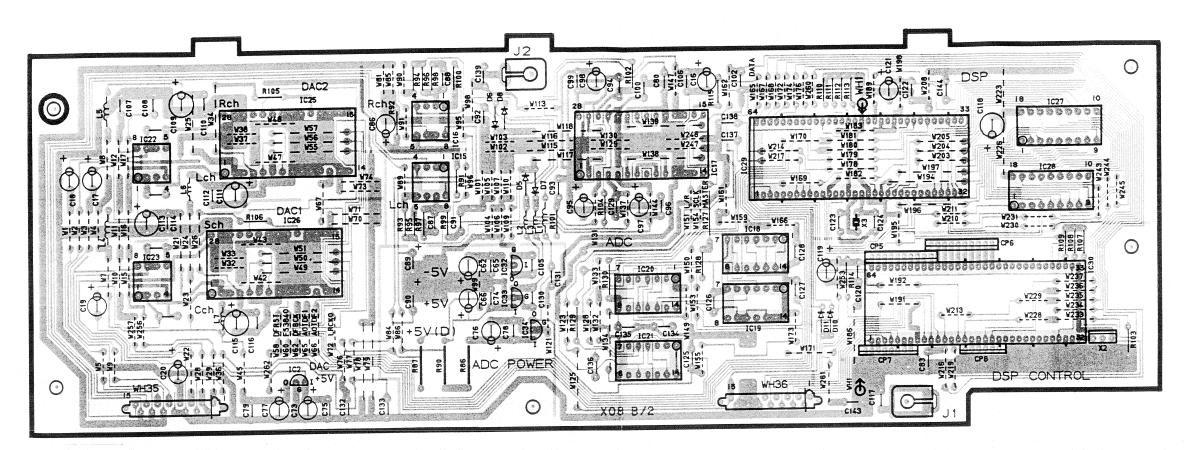
System connections

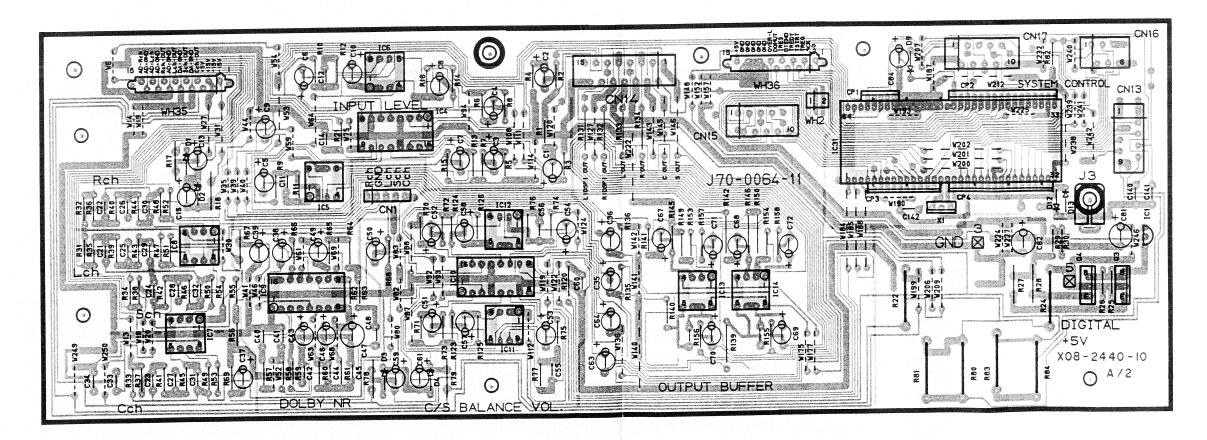


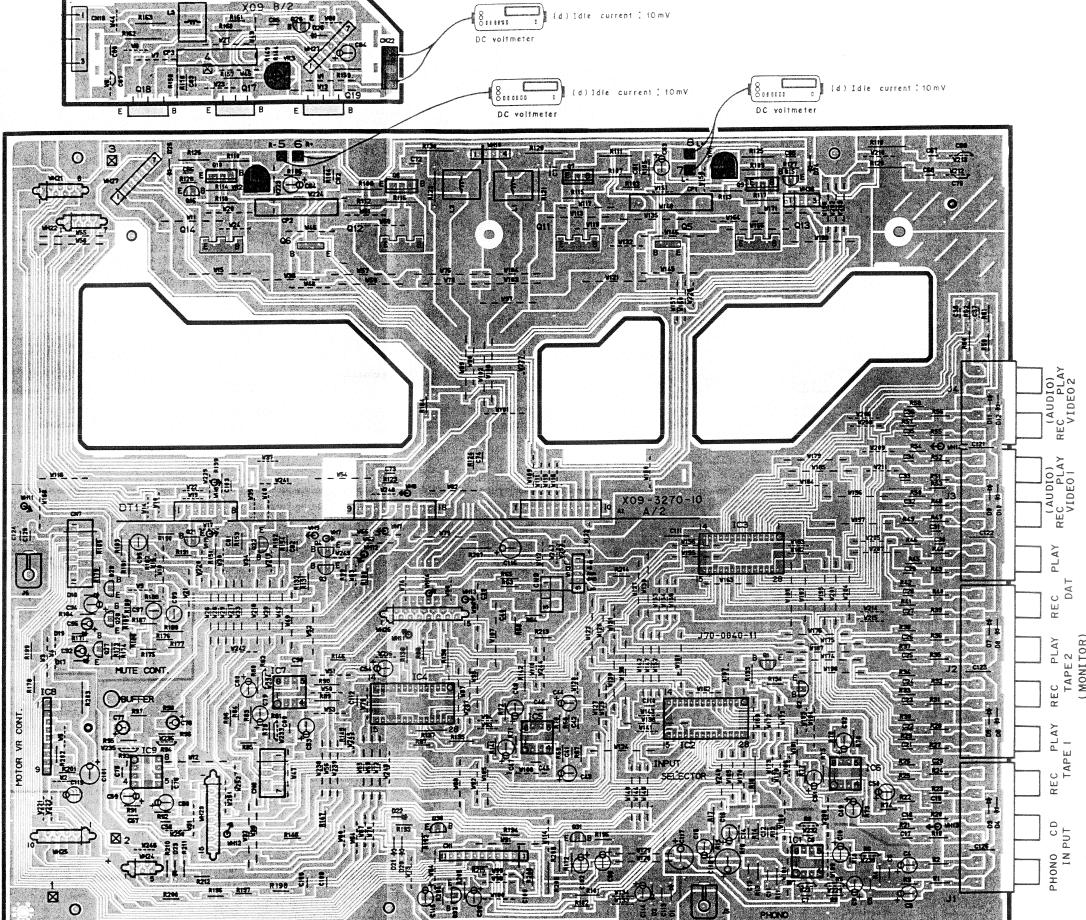


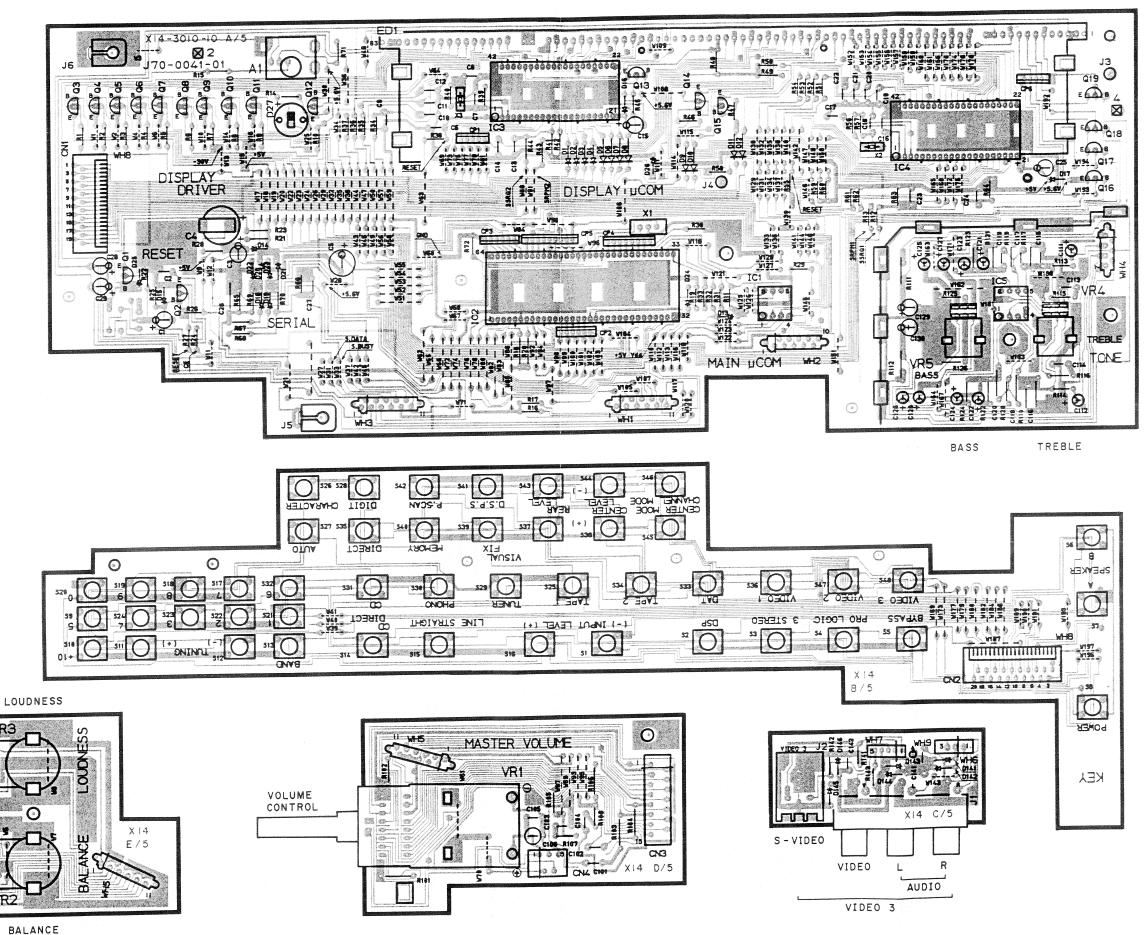






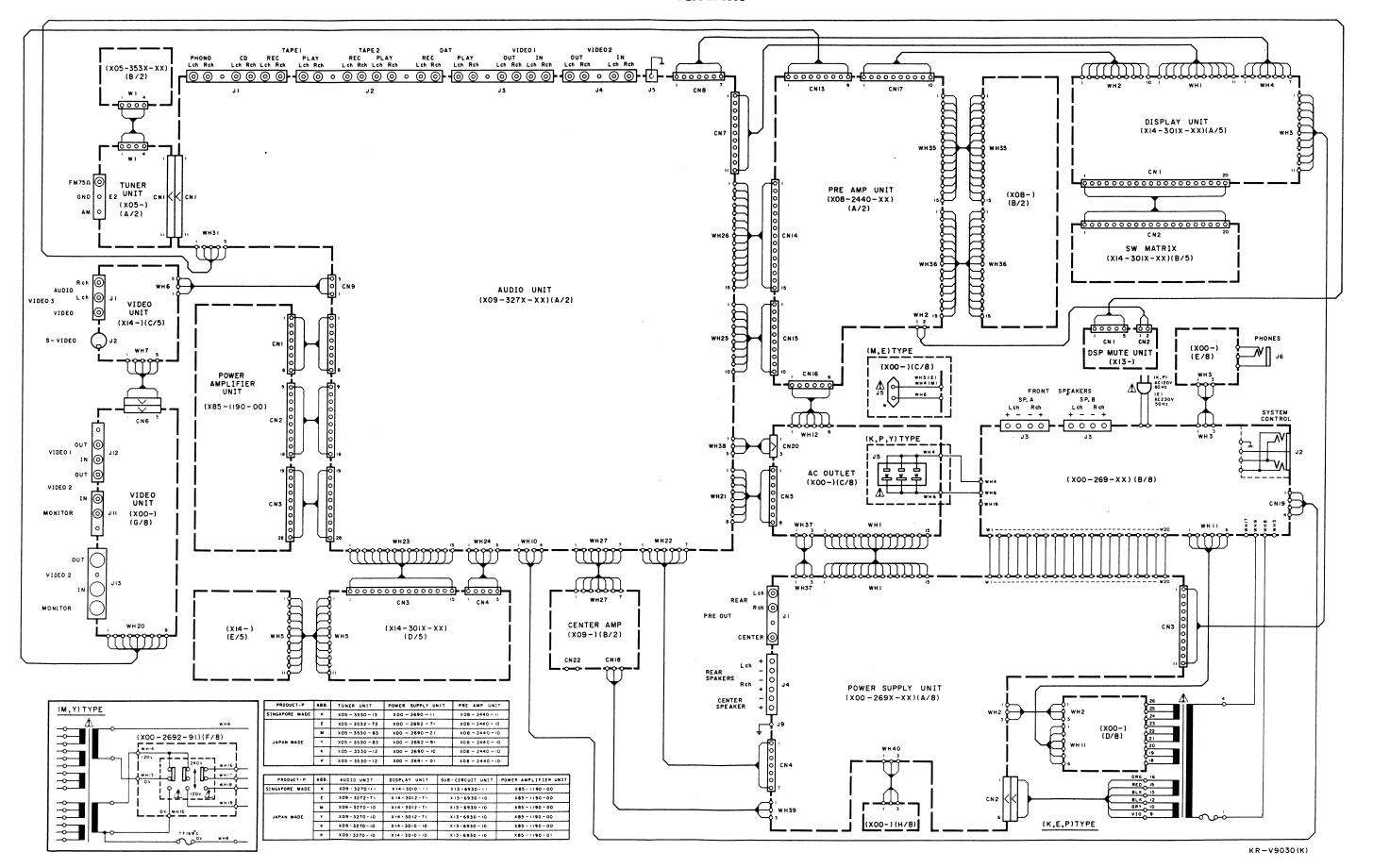




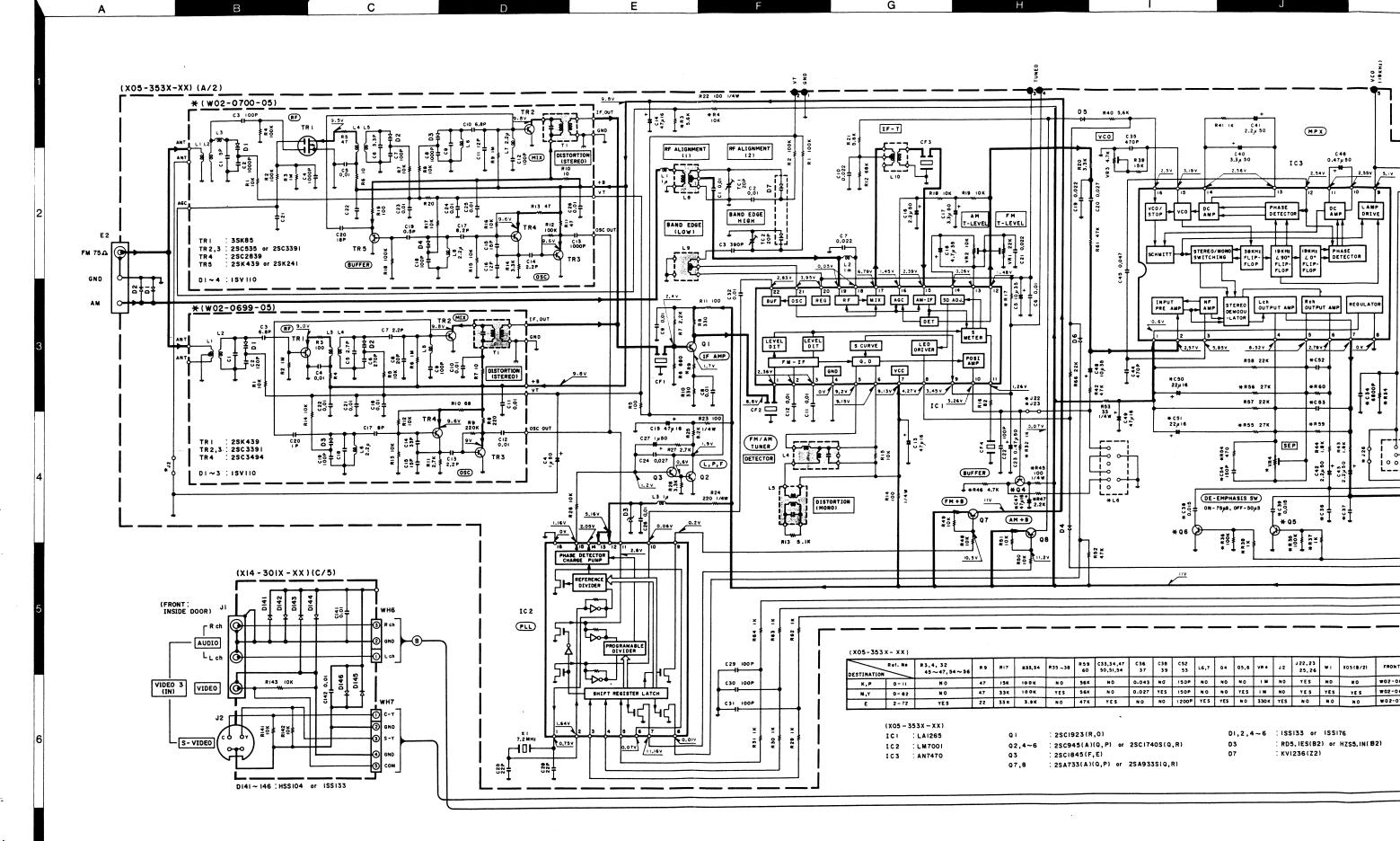


KR-V9030 KR-V9030

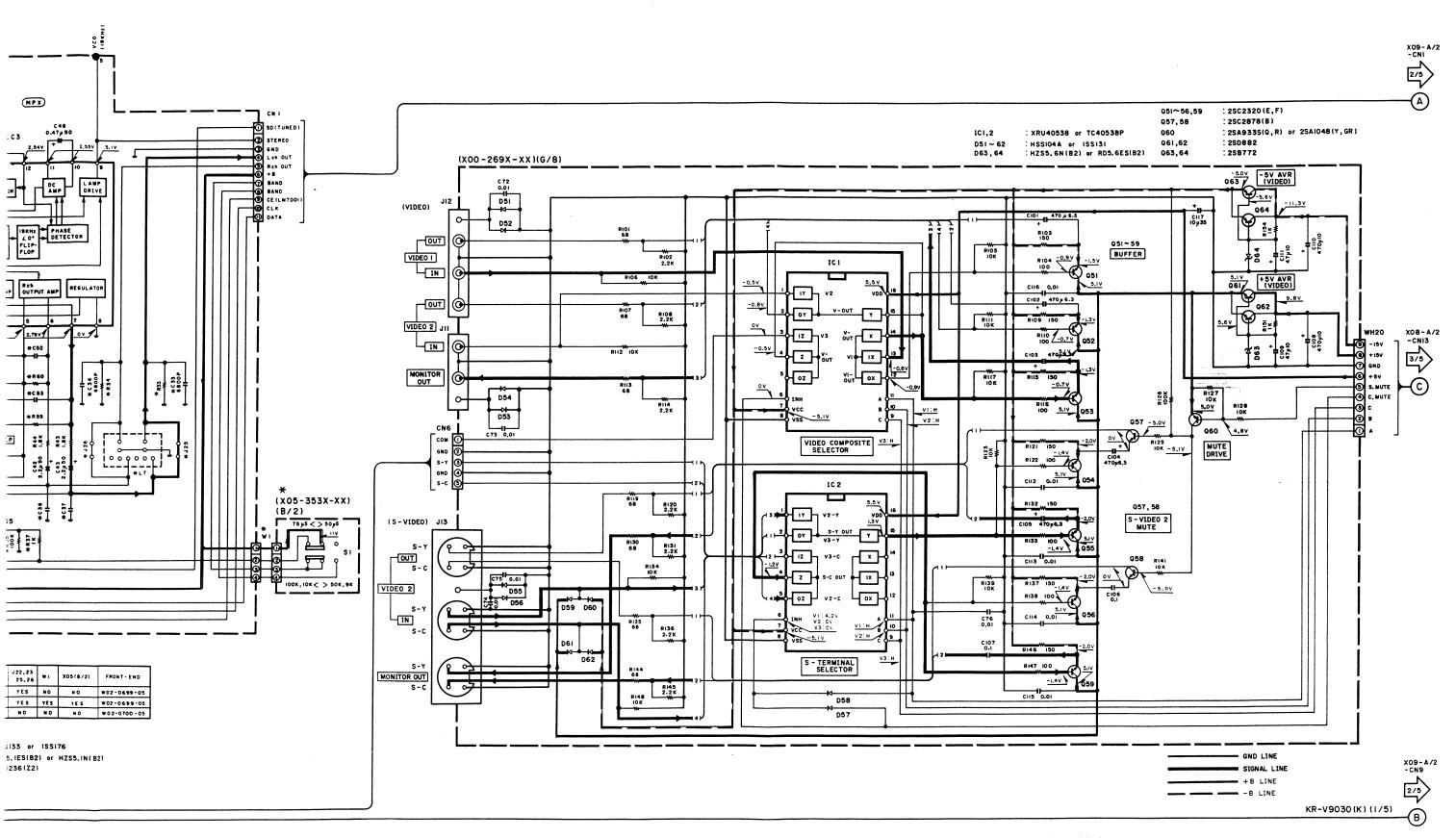
WIRING DIAGRAM



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G



0

DC voltages are as measured with a high impedance voltmeter with no signal input. Values may vary slightly due to variations between individual instruments or/and units.

М

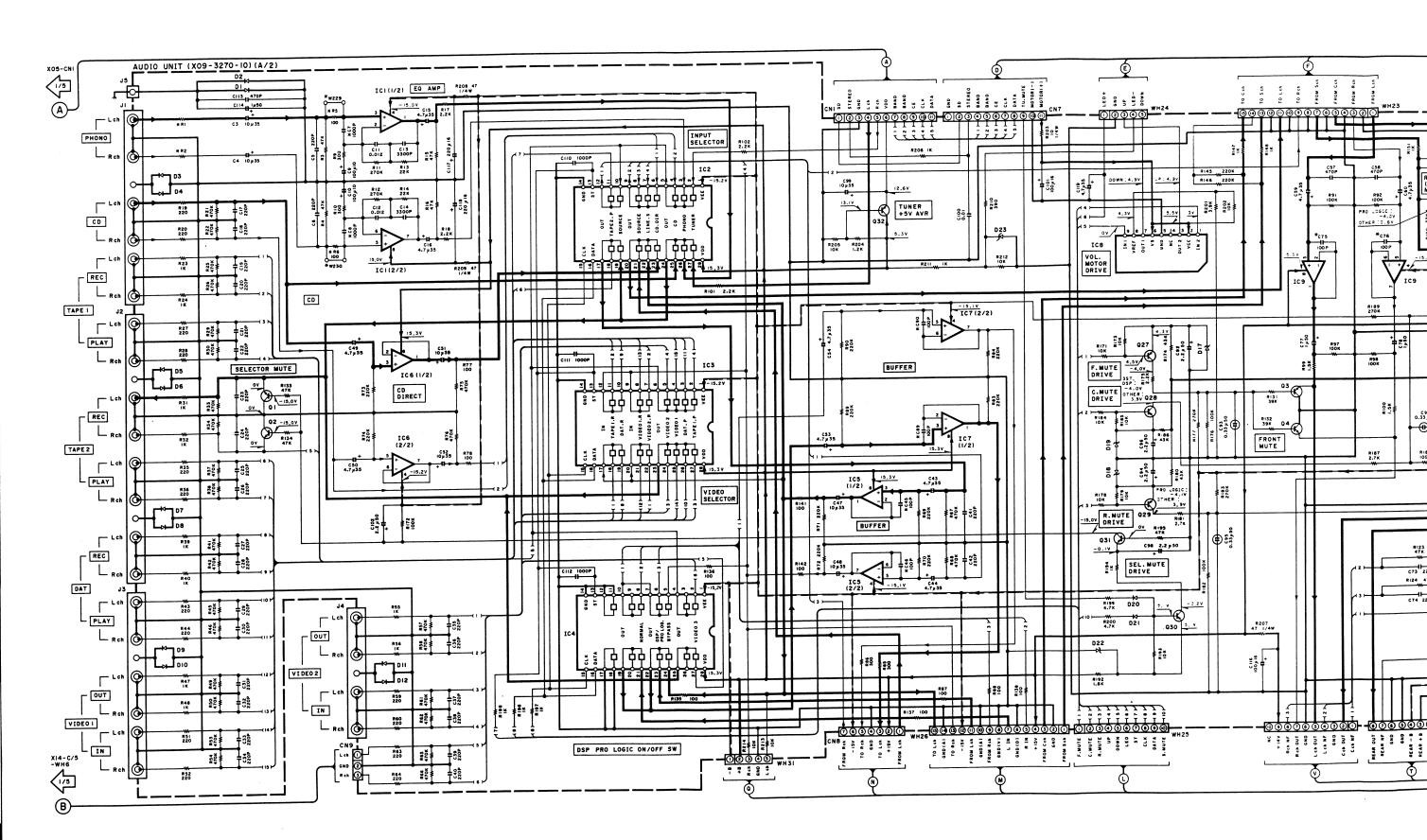
CAUTION: For continued safety, replace safety critical components only with manufacture's recommended parts (refer to parts list). △ Indicates safety critical components. To reduce the risk of electric shock, leakage-current or resistance measurements shall be carried out (exposed parts are acceptably insulated from the supply circuit) before the appliance is returned to the customer.

Q

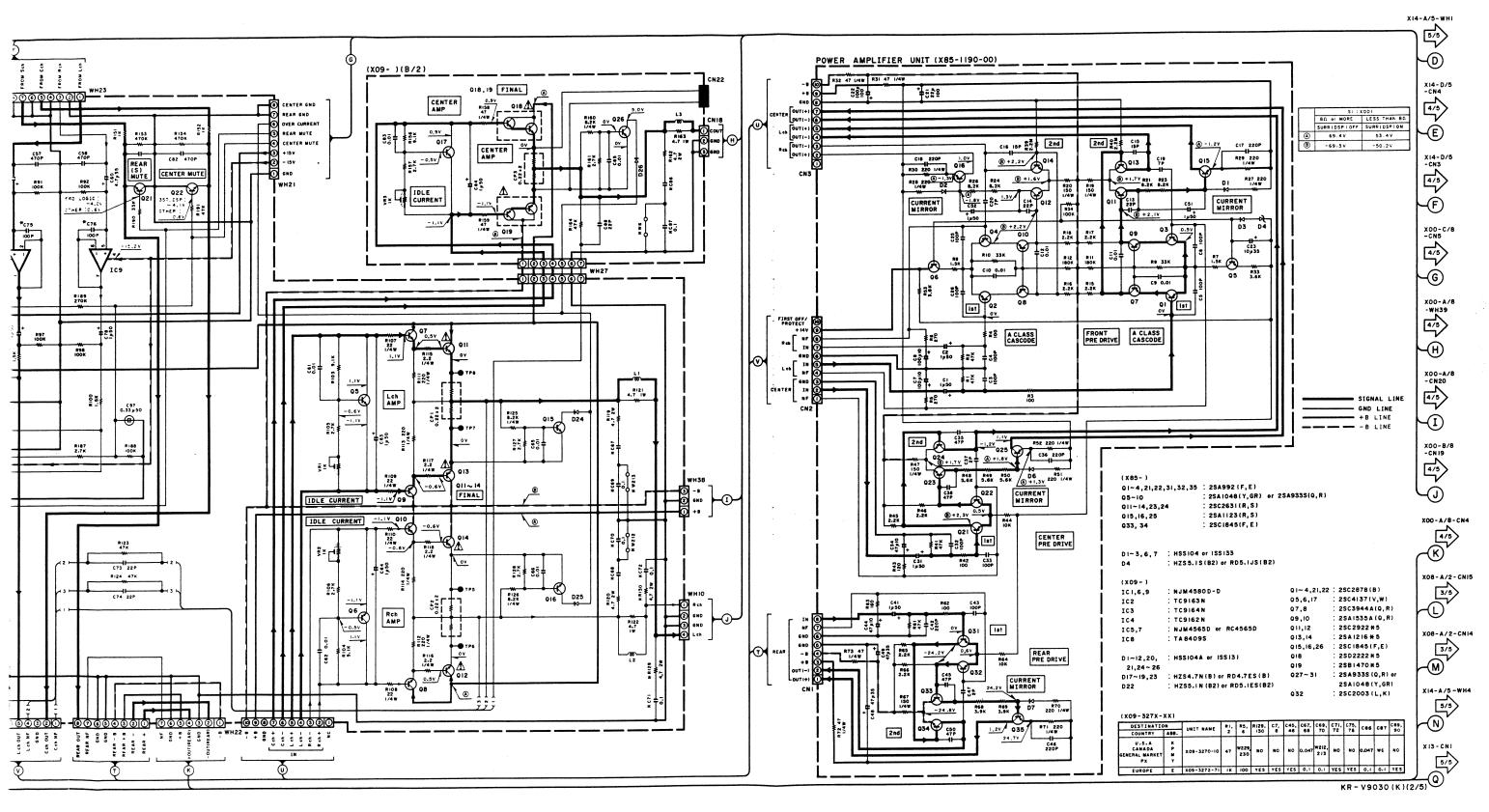


S

AC AD



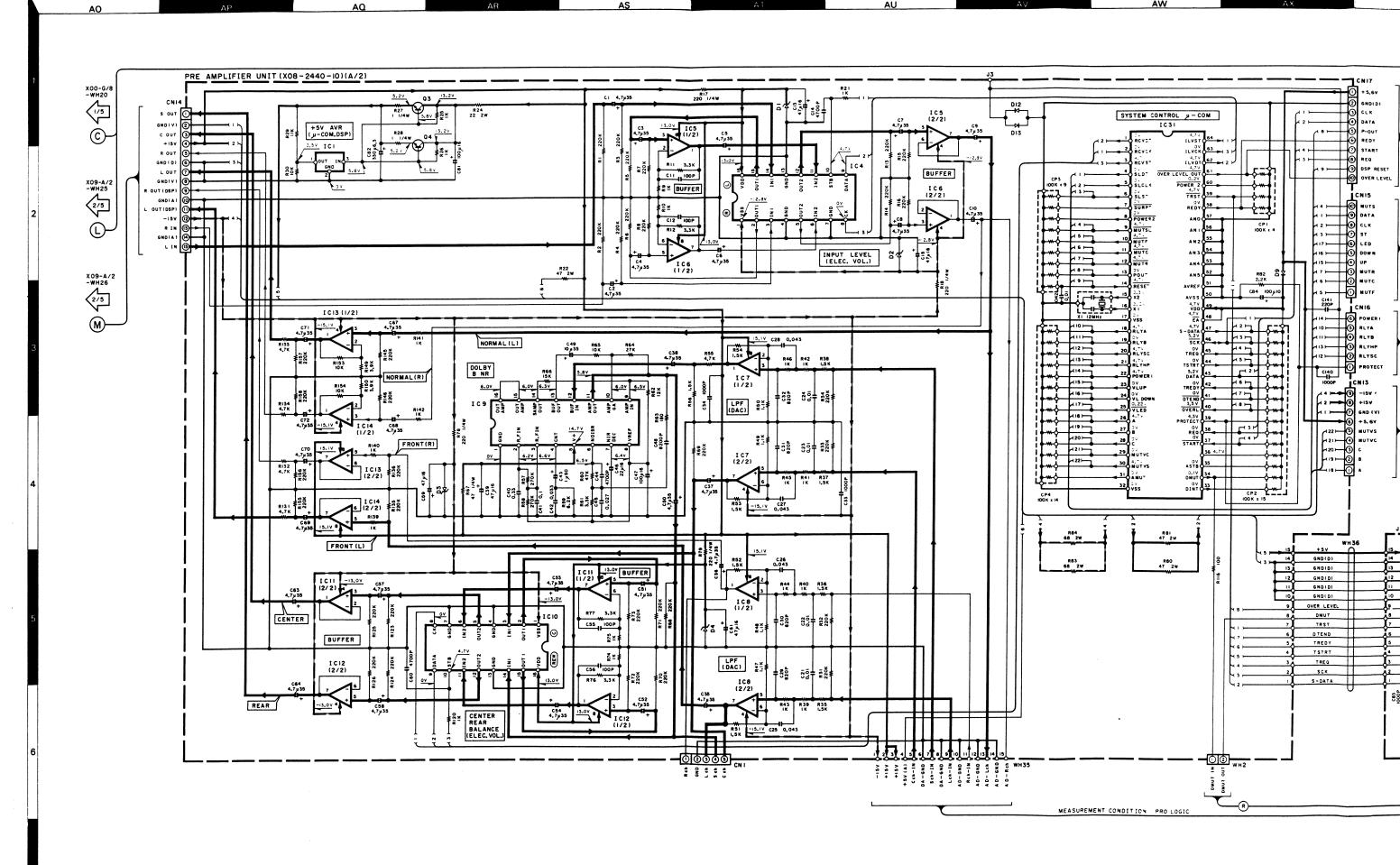


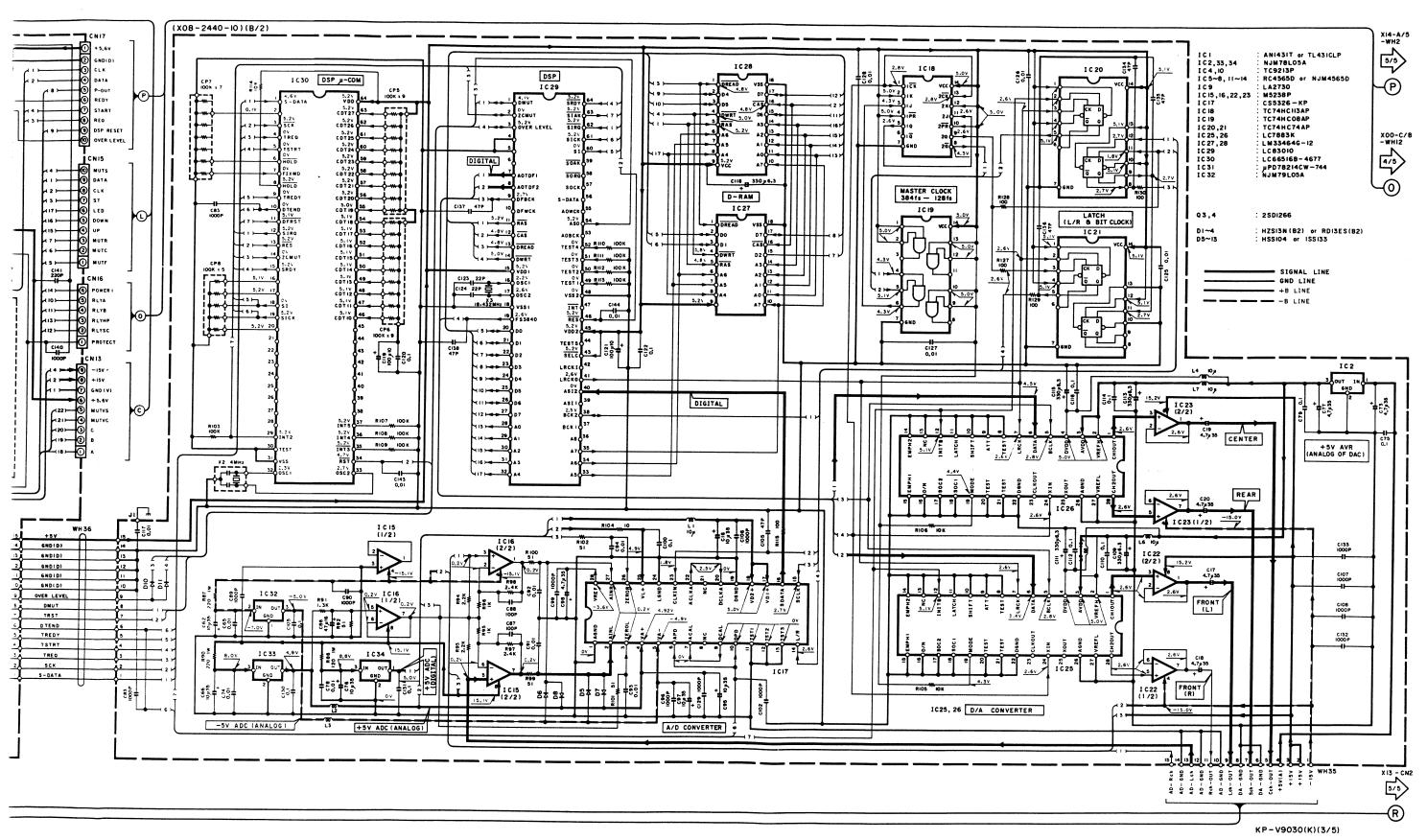


DC voltages are as measured with a high impedance voltmeter with no signal input. Values may vary slightly due to variations between individual instruments or/and units.

CAUTION: For continued safety, replace safety critical components only with manufacture's recommended parts (refer to parts list). Δ Indicates safety critical components. To reduce the risk of electric shock, leakage-current or resistance measurements shall be carried out (exposed parts are acceptably insulated from the supply circuit) before the appliance is returned to the customer.







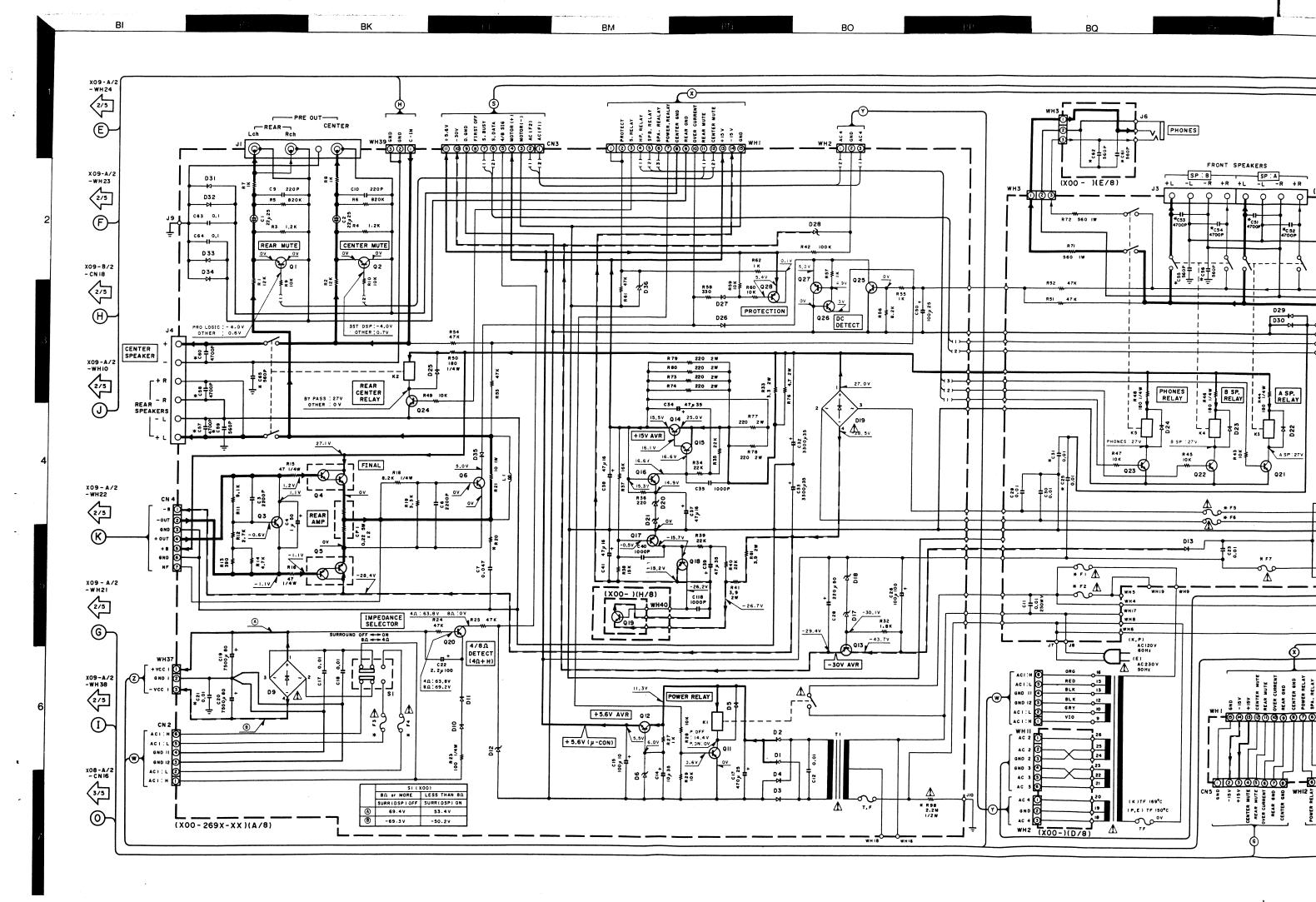
DC voltages are as measured with a high impedance voltmeter with no signal input. Values may vary slightly due to variations between individual instruments or/and units.

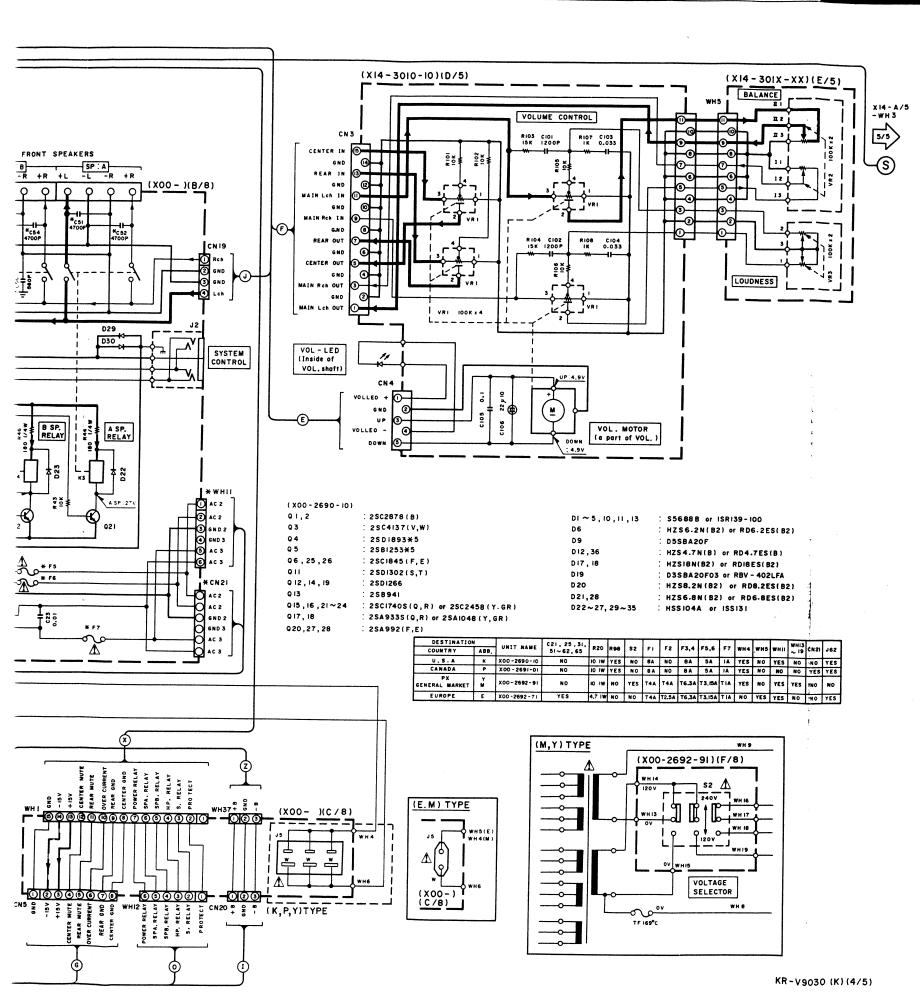
CAUTION: For continued safety, replace safety critical components only with manufacture's recommended parts (refer to parts list). Δ Indicates safety critical components. To reduce the risk of electric shock, leakage-current or resistance measurements shall be carried out (exposed parts are acceptably insulated from the supply circuit) before the appliance is returned to the customer.

KR-V9030

KENWOOD

Y05-2510-10





2SA1123 2SA733 (A) 2SA992 2SC1845 2SC1923 2SC2003 DTA124ES RN2203 2SA1048

2SD1266 2SA933S 2SC1740S 2SC2458 2SC4137

DC voltages are as measured with a high impedance voltmeter with no signal input. Values may vary slightly due to variations between individual instru-

caution: For continued safety, replace safety critical components only with manufacture's recommended parts (refer to parts list). A Indicates safety critical components. To reduce the risk of electric shock, leakage-current or resistance measurements shall be carried out (exposed parts are acceptably insulated from the supply circuit) before the appliance is returned to the customer

2SB1470*5 2SD2222*5



2SA1535A 2SB941 2SC3944A

2SD1893*5

NJM4565D

NJM4565D-A NJM4565D-D

TC74HC74AP

LA2730

LM7001

2SB772 2SD882



2SC2320

2SC2631

2SC2878

2SC945 (A) 2SD1302



2SB1253*5





2SA1216*5 2SC2922*5





TC4053BP TC9213P

AN7470



TA8409S



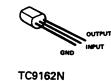
M5238P RC4565D RC4565D-D



AN1431T NJM78L05A



NJM79L05A



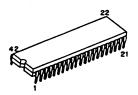
TC9163N TC9164N



LA1265



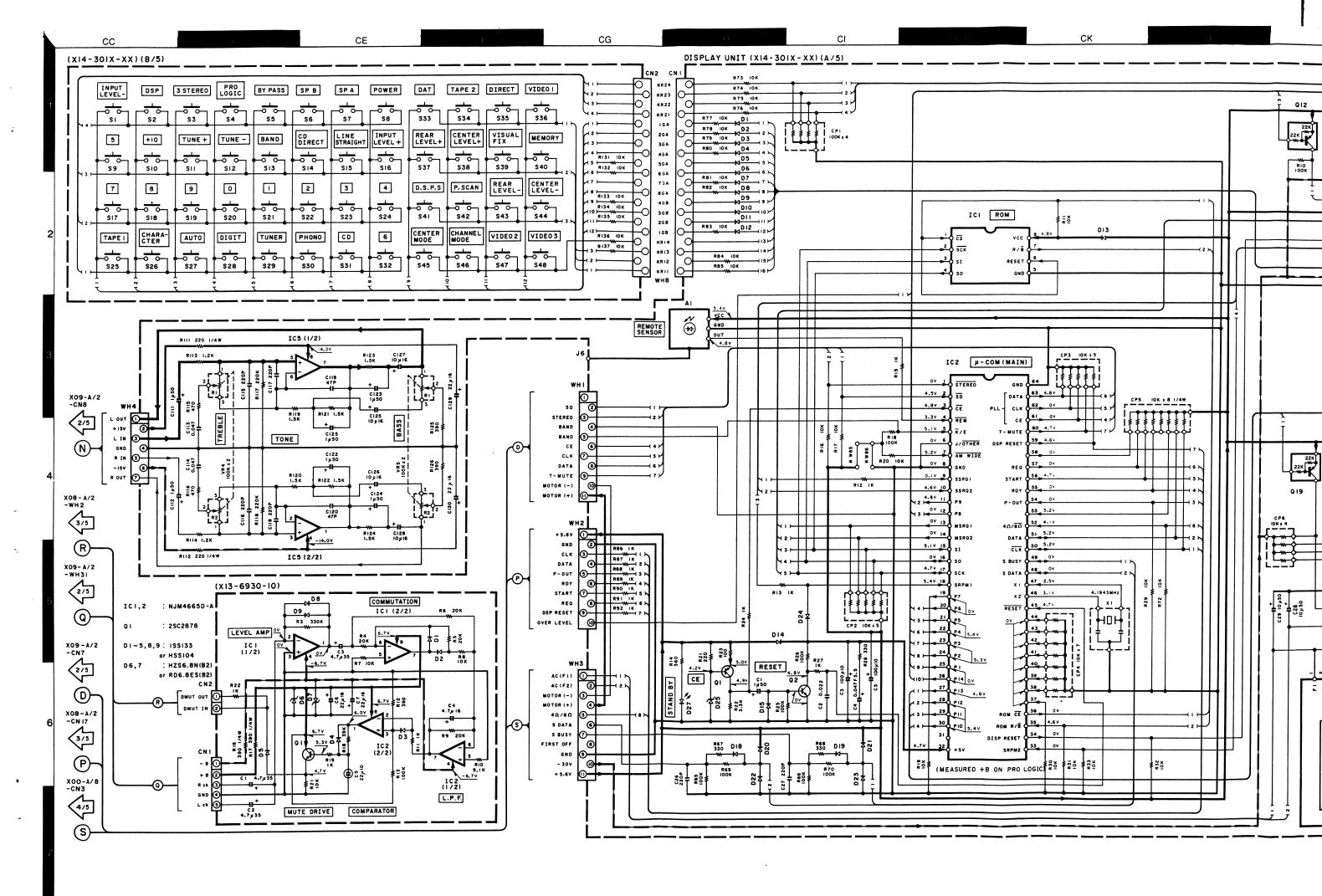
μPD7537ACU-220



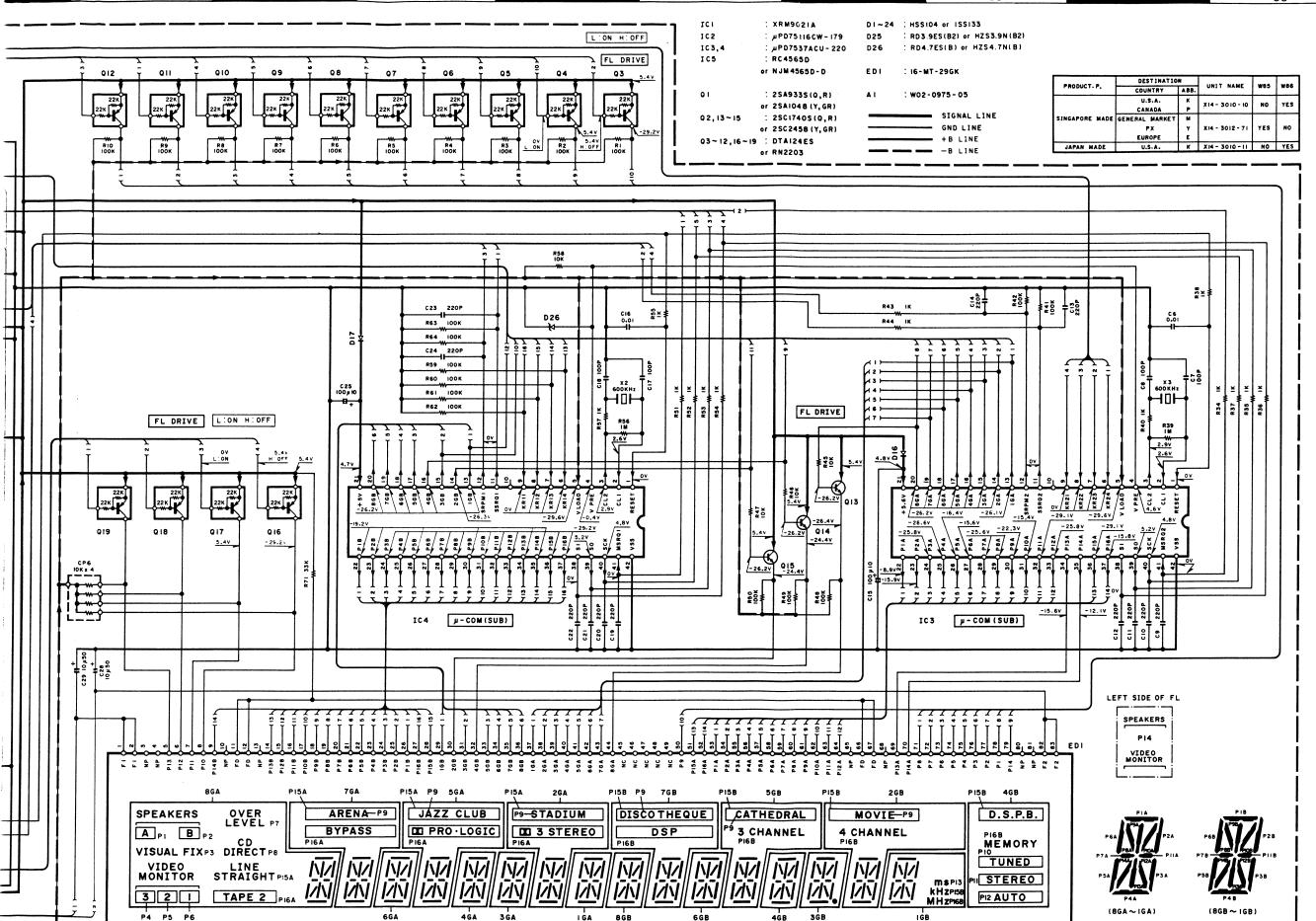
μPD75116CW-179







i



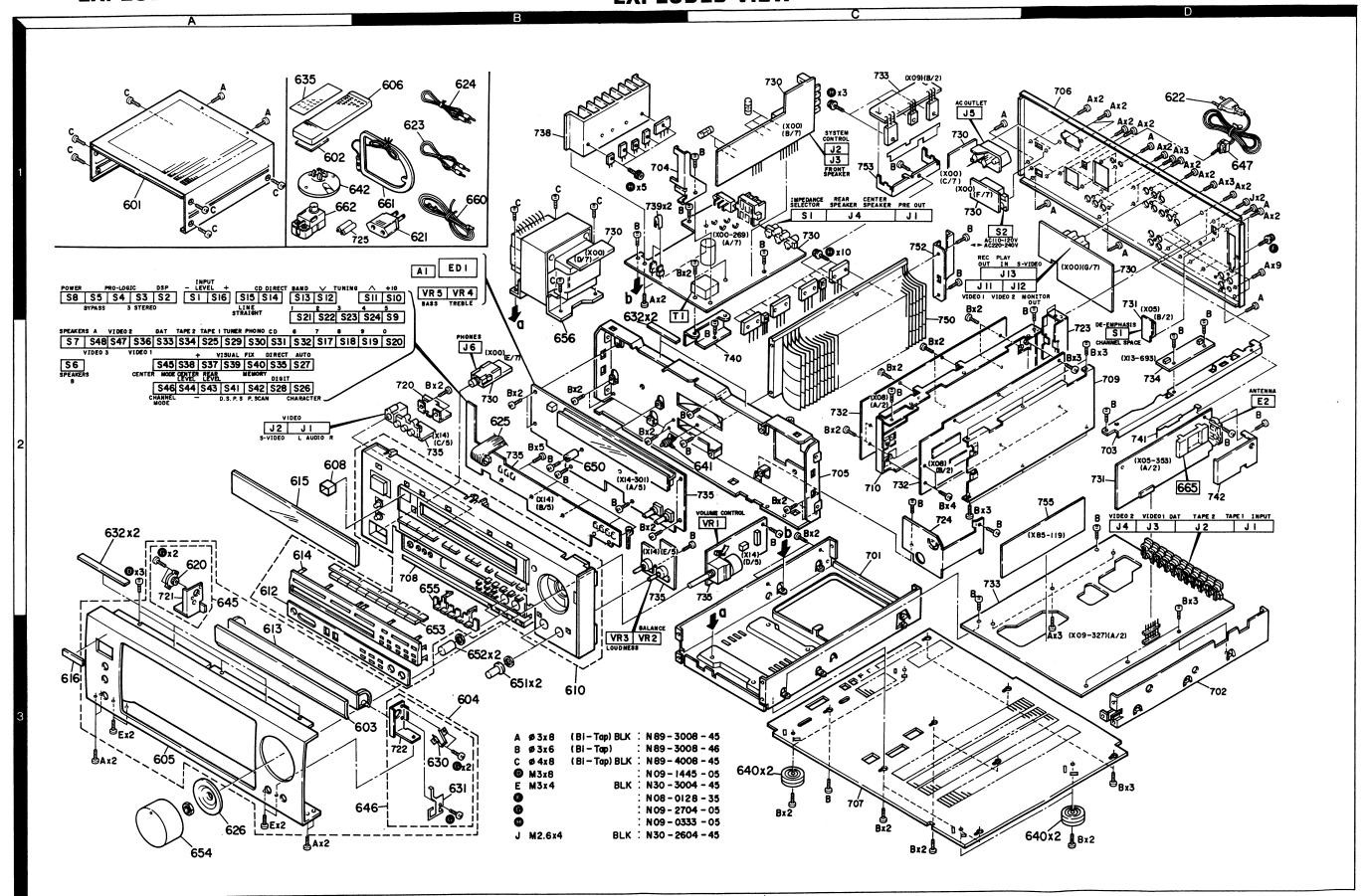
red with a high impedance input. Values may vary between individual instru-DC voltages are as measurer voltmeter with no signal in slightly due to variations be ments or/and units.

CAUTION: For continued safety, replace safety critical components only with manufacture's recommended parts (refer to parts list). A Indicates safety critical components. To reduce the risk of electric shock, leakage-current or resistance measurements shall be carried out (exposed parts are acceptably insulated from the supply circuit) before the appliance is returned to the customer.

KR-V9030 KR-V9030(K)(5/5) KENWOOD

KR-V9030 KR-V9030 EXPLODED VIEW

EXPLODED VIEW



Ref. 参照

616 ---

620

624 625 626

69

Telle ohne Parts No. werden nicht geliefert.

		_	Υ		,		,	Telle onne Parts	No. Werde	en nic	int gellerert.		N	0.Z
. No.	Address	New Part:	Parts No.	Description	Desti-	Re-		Ref. No.	Address			Description	Desti-	Re-
照番号	位置	新	* * * *	都品名/規格	nation 仕 库	備考		参照者号	位置	Perts	***	部 品 名/規 格	nation 仕 向	marks 僧考
		ŀ	(R-V9030 (SI	SAPORE MADE)			١.	646	3B		J21-5685-14	MOUNTING HARDWARE ASSY		
	1 A 1 A 1 A 3 B	*	A01-1806-21 A09-0088-08 A09-0111-08 A29-0182-02	METALLIC CABINET BATTERY COVER (A70-0506-05) BATTERY COVER (A70-0504-05) PANEL	E KPMY		A	650 -	10 2B	*	J42-0083-05 J39-0161-04 J61-0307-05	POWER CORD BUSHING SPACER WIRE BAND		
	3B 3A 1B 1B 2A	* * * * *	A60-0005-21 A60-0006-12 A70-0504-05 A70-0506-05 A33-0120-04	PANEL ASSY PANEL REMOTE CONTROLLER ASSY REMOTE CONTROLLER ASSY REFLECTOR	KPMY E			651 652 653 654 655	3B 3B 3B 3A 2B	* *	K29-3632-04 K29-3663-04 K29-4095-12 K29-4097-04 K29-4101-23	KNOB (LOUDNESS, BALANCE) KNOB (BASS, TREBLE) KNOB (INPUT SELECTOR) KNOB ASSY (VOLUME CONTROL) KNOB (10 KEY)		
	3B 2A 3A 2A 2A	* * * * *	B01-0474-32 B03-2684-03 B07-1972-02 B07-1973-12	PANEL ESCUTCHEON ASSY DRESSING PLATE ESCUTCHEON ESCUTCHEON			1 40	656 656 656	2B 2B 2B 2B 2B	*	L01-8621-05 L01-8625-05 L01-8627-05 L07-0275-05 N89-3008-45	POWER TRANSFORMER POWER TRANSFORMER POWER TRANSFORMER POWER TRANSFORMER BINDING HEAD TAPTITE SCREW	K MY P E	
	3A	*	B43-0287-04 B46-0092-03 B46-0094-03 B46-0095-03	FRONT GLASS KENWOOD BADGE WARRANTY CARD WARRANTY CARD WARRANTY CARD	K Y Y			B C D E			N89-3008-46 N89-4008-45 N09-1445-05 N30-3004-45	BINDING HEAD TAPTITE SCREW BINDING HEAD TAPTITE SCREW SET SCREW (M3X8) PAN HEAD MACHIN SCREW		
			B46-0121-03 B46-0122-13 B58-0513-04	WARRANTY CARD WARRANTY CARD CAUTION CARD (PRESET220-240)	P B Y			F G J 660	1B		N08-0128-35 N09-2704-05 N30-2604-45	BINDING POST TAPTITE SCREW PAN HEAD MACHIN SCREW	MY	
		*	860-0312-00 860-0313-00 860-0358-00	INSTRUCTION MANUAL(ENGLISH) INSTRUCTION MANUAL(FRENCH) INSTRUCTION MANUAL(S,C)	KPMY P M			661 662	19 18		T90-0174-05 T90-0177-05	T TYPE ANTENNA LOOP ANTENNA ANTENNA ADAPTOR APAN MADE)		
		*	B60-0359-10	INSTRUCTION MANUAL(F,G,D)	E			601 602	1 A 1 A	*	A01-1806-21 A09-0111-08	METALLIC CABINET		
	2A	İ	D39-0200-05	DAMPER				603 604	3B 3B	*	A29-0111-08 A29-0182-02 A60-0005-21	BATTERY COVER (A70-0504-05) PANEL		
	1B 1D 1D 1D 1B		E03-0115-05 E30-0459-05 E30-0812-05 E30-2209-05 E30-0977-05	AC PLUG ADAPTER AC POWER CORD AC POWER CORD AC POWER CORD CORD WITH PLUG	ME Y KP E			606 608	3A 1B 2A	* * *	A70-0504-05 A33-0120-04	PANEL ASSY PANEL REMOTE CONTROLLER ASSY REFLECTOR		
-	1 B 2 B	*	E30-1392-05 E35-0111-05	CORD WITH PLUG WIRING HARNESS	E			610 612 613	3B 2A 3A	* *	B01-0474-32 B03-2684-03 B07-1972-02	PANEL ESCUTCHEON ASSY DRESSING PLATE		
	3A	*	F10-0805-04	SHIELDING PLATE				614	2A 2A	*	B07-1973-12 B10-1837-13	ESCUTCHEON ESCUTCHEON FRONT GLASS		
	3B 3B 2A,2B 1A	* *	G02-0981-04 G02-0982-14 G10-0148-04 G16-0772-08	FLAT SPRING FLAT SPRING NON-WOVEN FABRIC WRITING SHEET (A70-0504-05)	KPMY			616	3A	*	B43-0287-04 B46-0092-03 B60-0312-00	KENWOOD BADGE WARRANTY CARD INSTRUCTION MANUAL(ENGLISH)		
		*	H10-5082-02 H10-5083-02	POLYSTYRENE FOAMED FIXTURE(L) POLYSTYRENE FOAMED FIXTURE(R)				620	2A		D39-0200-05	DAMPER		
		*	H11-0033-04 H12-2099-04 H25-0225-04	POLYSTYRENE FOAMED BOARD PACKING FIXTURE PROTECTION BAG (850X450X0.03)			Δ	625	1D 2B	*	E30-0974-05 E35-0111-05	AC POWER CORD WIRING HARNESS		
	30	*	H25-0232-04 H50-0004-04 H50-0005-04	PROTECTION BAG (235X350X0.03) ITEM CARTON CASE ITEM CARTON CASE	KPMY E			626 630 631 632 635	3A 3B 3B 2A, 2B	* * * *	F10-0805-04 G02-0981-04 G02-0982-14 G10-0148-04 C14-0772-08	SHIELDING PLATE FLAT SPRING FLAT SPRING NON-WOVEN FABRIC		
	3C,3D 2C 1B 2A	*	J02-1002-05 J19-0506-05 J19-2815-04 J21-5683-14	FOOT UNIT HOLDER ANTENNA HOLDER MOUNTING HARDWARE ASSY				- - -	1 A	*	G16-0772-08 H10-5149-02 H10-5150-02 H11-0035-04	WRITING SHEET (A70-0504-05) POLYSTYRENE FOAMED FIXTURE(L) POLYSTYRENE FOAMED FIXTURE(R) POLYSTYRENE FOAMED BOARD		
E: Scand	inavia & E	urop	e K: USA	P: Canada			•	E: Scan	dinavia & E		e K: USA	P: Canada		

Y: PX(Far East, Hawaii) Y: AAFES (Europe)

T: England

M: Other Aeas

X: Australia

★ indicates safety critical components.

T: England

M: Other Aeas

Y: PX(Far East, Hawaii) X: Australia

⚠ indicates safety critical components.

6

→ New Parts
Parts without Parts No. are not supplied.

Les articles non mentionnes dans le Parts No. ne sont pas fournis.

Ref. No. 参照者号	Address 位 置	New Parts 新	Parts No. 部品音号	Description 部 品 名 / 規 格	nation	Re- mark 備考
- - -		*	H12-2102-04 H25-0225-04 H25-0232-04 H50-0088-04	PACKING FIXTURE PROTECTION BAG (850X450X0.03) PROTECTION BAG (235X350X0.03) ITEM CARTON CASE		
640 641 642 645 646	3C,3D 2C 1B 2A 3B	* *	J02-1002-05 J19-0506-05 J19-2815-04 J21-5683-14 J21-5685-14	FOOT UNIT HOLDER ANTENNA HOLDER MOUNTING HARDWARE ASSY MOUNTING HARDWARE ASSY		
647 650	1 D 2 B	*	J42-0083-05 J39-0161-04 J61-0307-05	POWER CORD BUSHING SPACER WIRE BAND		
651 652 653 654 655	3B 3B 3B 3A 2B	* *	K29-3632-04 K29-3663-04 K29-4095-12 K29-4097-04 K29-4101-23	KNOB (LOUDNESS, BALANCE) KNOB (BASS, TREBLE) KNOB (INPUT SELECTOR) KNOB ASSY (VOLUME CONTROL) KNOB (10 KEY)		
656	2B		L01-8621-05	POWER TRANSFORMER		
A B C D E			N89-3008-45 N89-3008-46 N89-4008-45 N09-1445-05 N30-3004-45	BINDING HEAD TAPTITE SCREW BINDING HEAD TAPTITE SCREW BINDING HEAD TAPTITE SCREW SET SCREW (M3XB) PAN HEAD MACHIN SCREW		
F G			N08-0128-35 N09-2704-05	BINDING POST TAPTITE SCREW		
660 661 662	18 18 18		T90-0175-05 T90-0173-05 T90-0177-05	T TYPE ANTENNA Loop Antenna Antenna Adaptor		
	P	OV	VER SUPPLY	UNIT (X00-2690-10)		
C1 ,2 C3 C4 C6 C7			C90-1353-05 CK45FB1H222K CE04LW1H010M CK45FB1H222K CF92FV1H473J	NP-BLEC 22UF 25WV CERAMIC 2200PF K ELECTRO 1.0UF 50WV CERAMIC 2200PF K MF 0.047UF J		
C9 ,10 C11 C12 C13 C14			CC45FSL1H221J C91-0971-05 CK45FF1H103Z CE04LW1E471M CE04LW1V100M	CERAMIC 220PF J FILM 0.01UF 250WV CERAMIC 0.010UF Z ELECTRO 470UF 25WV ELECTRO 10UF 35WV		
C15 C17 ,18 C19 ,20 C21 C22		*	CE04LW1A101M CK45FF1H103Z C90-1869-05 CK45FF1H103Z CE04LW2A2R2M	BLECTRO	Е	
C23 C25 C26 C28 C29 ,30			CK45FF1H103Z CK45FF1H103Z CE04LW2A101M CE04LW1H221M CK45FF1H103Z	CERAMIC 0.010UF Z CERAMIC 0.010UF Z ELECTRO 100UF 100WV ELECTRO 220UF 50WV CERAMIC 0.010UF Z	Е	
C31 C32,33 C34 C35			CK45FF1H103Z CE04LW1V332M CE04LW1V470M CK45FB1H102K	CERAMIC 0.010UF Z ELECTRO 3300UF 35WV ELECTRO 47UF 35WV CERAMIC 1000PF K	Е	

E: Scandinavia & Europe Y: PX(Far East, Hawaii)

Y: AAFES (Europe)

K: USA T: England

X: Australia

P: Canada

M: Other Aeas

★ indicates safety critical components.

× New Parts Parts without Parts No. are not supplied.

l	Ref.	No.	Address		Parts No.	Description		Re-
L	# M	# 7	位置	Perte Si	* * * *	部 品 名/規 格	nation 仕 南	mar l
	C37 (, 38			CEO4LW1C470M CEO4LW1V470M	ELECTRO 47UF 16WV ELECTRO 47UF 35WV		
	C40				CK45FB1H102K	CERANIC 1000PF K		1
	C41 C50				CEO4LW1C470M CEO4LW1E101M	ELECTRO 47UF 16WV ELECTRO 100UF 25WV		
		-54			CK45FF1H472Z	CERAMIC 4700PF Z	E	
	C57 ,	56 58		l	CK45FB1H561K CF92FV1H472J	CERAMIC 560PF K	E	
	C59			ll	CK45FB1H561K	CERAMIC 560PF K		
	C60				CF92FV1H472J	MF 4700PF J	E	
	C61 ,	62			CK45FB1H561K CK45FF1H103Z	CERAMIC 560PF K CERAMIC 0.010UF Z	E	
	C65			1	CK45FB1H561K	CERAMIC 560PF K	E	
	C72 - C76	-75			C91-0769-05 CK45FF1H103Z	CERAMIC 0.01UF K CERAMIC 0.01UF Z		
	C101-				CEO4LWOJ471M	ELECTRO 470UF 6.3WV		
	C106, C108	107			CF92FV1H104J CE04LW1A471M	MF 0.10UF J ELECTRO 470UF 10WV		
	C109 C110				CEO4LW1A470M CEO4LW1A471M	ELECTRO 47UF 10WV ELECTRO 470UF 10WV		
	C111				CEO4LW1A470M	ELECTRO 47UF 10WV		
	C112-	-116			CK45FF1H103Z	CERAMIC 0.010UF Z		
	C117 C118				CEO4LW1V100M CK45FB1H102K	ELECTRO 10UF 35WV CERAMIC 1000PF K		
	J1		1C		E13-0308-05	PHONO JACK (PRE OUT)		
	J2 J3		1C 1C		E11-0188-05 E20-0823-05	MINIATURE PHONE JACK(S.CONT) LOCK TERMINAL BOARD (F.SP)		
	J4		1C	*	E70-0001-05	LOCK TERMINAL BOARD (C,R SP)		l
	J5 **		1C		E03-0108-05	AC OUTLET	ME	
	J5 J6		1C 2B		E03-0111-05 E11-0189-05	AC QUTLET PHONE JACK (PHONES)	KPY	
	J11		1C 1C		E13-0291-05	PHONO JACK(VIDEO2 IN, MONITOR)		ŀ
	J12 J13		10		E13-0318-05 E06-0407-05	PHONO JACK(VIDEO1 OUT, IN) CYLINDRICAL RECEPTACLE(VIDEO)		
	F1				F05-4025-05	FUSE (SEMKO) (250V T4A)	MYE	
	F1 F2				F05-8029-05 F05-2525-05	FUSE (UL) (250V 8A) FUSE (SEMKO) (250V T2.5A)	KP E	
	F2				F05-4025-05	FUSE (SEMKO) (250V T4A)	MY	ļ
		4			F05-6321-05	FUSE (SEMKO) (250V T6.3A)	MYE	İ
	F5 ,	6			F05-8029-05 F04-5022-05	FUSE (UL) (250V 8A) FUSE (UL) (125V 5A UL)	KP KP	ľ
	F5,	6		ا ا	F05-3121-05	FUSE (SEMKO) (250V T3.15A)	MYE	
	F7 F7			*	F04-1026-05 F06-1022-05	FUSE (UL) (250V 1A) FUSE (SEMKO) (250V T1A)	KP Mye	
	CN51,	52			J13-0041-05	FUSE CLIP	KP	
	CN51, CN53,				J13-0075-05 J13-0075-05	FUSE CLIP FUSE CLIP	MYE	
	CN55-	-58			J13-0041-05	FUSE CLIP	KP	
	CN55-				J13-0075-05	FUSE CLIP	MYE	
	CN59- J14	-64			J13-0075-05 J11-0098-05	FUSE CLIP WIRE CLAMPER		
	L1			$ \ $	L39-0085-05	PHASE-COMPENSATION COIL		
	T1 T1		2B 2B	ll	L01-7651-05 L01-7653-05	POWER TRANSFORMER POWER TRANSFORMER	KP MY	1

E: Scandinavia & Europe

K: USA

P: Canada

Y: PX(Far East, Hawaii) Y: AAFES (Europe)

T: England X: Australia M: Other Aeas

⚠ indicates safety critical components.

Les articles non mentionnes dans le Parts No. ne sont pas fournis.

Telle ohne Parts No. werden nicht geliefert.

No.5

ſ	Ref. No.	Address	New	Parts No.	Description	Desti- Re-
	参照者号	位 置	Parts Si	部品番号	部 品 名/規 格	nation marks 仕 向 備考
Δ	T1	2B		L01-7657-05	POWER TRANSFORMER	Е
	A B H			N89-3008-45 N89-3008-46 N09-0333-05	BINDING HEAD TAPTITE SCREW BINDING HEAD TAPTITE SCREW TAPPING SCREW (3X12)	
	CP1 R15 .16 R18 R20 R20			R90-0187-05 RD14NB2E470J RD14NB2E822J RS14KB3A100J RS14KB3A4R7J	MULTI-COMP 0.22X2 K 5W RD 47 J 1/4W RD 8.2K J 1/4W FL-PROOF RS 10 J 1W FL-PROOF RS 4.7 J 1W	KPMY E
	R21 R23 R33 R41 R44		* *	RS14KB3A100J RD14NB2E101J RS14KB3D4R7J RS14KB3D3R9J RD14NB2E181J	FL-PROOF RS 10 J 1W RD 100 J 1/4W FL-PROOF RS 4.7 J 2W FL-PROOF RS 3.9 J 2W RD 180 J 1/4W	
	R46 R48 R50 R64 R71 ,72		* * *	RD14NB2E181J RD14NB2E181J RD14NB2E181J R92-0221-05 RS14KB3A561J	RD 180 J 1/4W RD 180 J 1/4W RD 180 J 1/4W FUSE RESIST 18 G 1/4W FL-PR00F RS 560 J 1W	
Δ	R73 ,74 R76 R77 -80 R81 R98		×	RS14KB3D221J RS14KB3D4R7J RS14KB3D221J RS14KB3D3R9J R92-0173-05	FL-PROOF RS 220 J 2W FL-PROOF RS 4.7 J 2W FL-PROOF RS 220 J 2W FL-PROOF RS 3.9 J 2W RC 2.2M M 1/2W	КР
∆	K1 K2 ,3 K4 ,5 S1 S2	1C 1C	*	\$76-0002-05 \$51-2078-05 \$76-0005-05 \$31-2136-05 \$31-3010-05	MAGNETIC RELAY MAGNETIC RELAY MAGNETIC RELAY SLIDE SWITCH(IMPEDANCE SELECT) SLIDE SWITCH(POWER TYPE)	MY
◭	D1 -5 D1 -5 D6 D6 D9		*	S5688B 1SR139-100 HZS6.2N(B2) RD6.2ES(B2) D5SBA20	DIODE DIODE ZENER DIODE ZENER DIODE DIODE	
	D10 ,11 D10 ,11 D12 D12 D13			S5688B 1SR139-100 HZS4.7N(B) RD4.7ES(B) S5688B	DIODE DIODE ZENER DIODE ZENER DIODE DIODE	
£	D13 D17 ,18 D17 ,18 D19 D19			1SR139-100 HZS18N(B2) RD18ES(B2) D3SBA20F03 RBV-402LFA	DIODE ZENER DIODE ZENER DIODE DIODE DIODE	
	D20 D20 D21 D21 D22 -27			HZS8.2N(B2) RD8.2ES(B2) HZS6.8N(B2) RD6.8ES(B2) HSS104A	ZENER DIODE ZENER DIODE ZENER DIODE ZENER DIODE DIODE	
	D22 -27 D28 D28 D29 -35 D29 -35			1SS131 HZS6.8N(B2) RD6.8ES(B2) HSS104A 1SS131	DIODE ZENER DIODE ZENER DIODE DIODE DIODE DIODE	

E:	Scandinavia	ō.	EL

Y: PX(Far East, Hawaii)

× New Parts

Parts without Parts No. are not supplied.

Les articles non mentionnes dans le Parts No. ne sont pas fournis.

Telle ohne Parts No. werden nicht geliefert.

No.6

Ref. No.	Address New		Description	Desti- Re-
参照者号	位置新		部品名/規格	nation mar 仕 向 備
D36 D36 D51 -62 D51 -62 D63 ,64		HZS4.7N(B) RD4.7ES(B) HSS104A 1SS131 HZS5.6N(B2)	ZENER DIODE ZENER DIODE DIODE DIODE ZENER DIODE	
D63 ,64 IC1 ,2 IC1 ,2 Q1 ,2	*	RD5.6ES(B2) TC4053BP XRU4053B 2SC2878(B) 2SC4137(V,W)	ZENER DIODE IC(3-INPUT 2CH MPX/DE-MPX) IC(3-INPUT 2CH MPX/DE-MPX) TRANSISTOR TRANSISTOR	
Q4 Q5 Q6 Q11 Q12		2SD1893*5 2SB1253*5 2SC1845(F,E) 2SD1302(S,T) 2SD1266	TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR	
Q13 Q14 Q15 ,16 Q15 ,16 Q17 ,18		2SB941 2SD1266 2SC1740S(Q,R) 2SC2458(Y,GR) 2SA1048(Y,GR)	TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR	
Q17 ,18 Q19 Q20 Q21 -24 Q21 -24		2SA933S(Q,R) 2SD1266 2SA992(F,E) 2SC1740S(Q,R) 2SC2458(Y,GR)	TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR	
Q25 ,26 Q27 ,28 Q51 -56 Q57 ,58 Q59		2SC1845(F,E) 2SA992(F,E) 2SC2320(E,F) 2SC2878(B) 2SC2320(E,F)	TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR	
960 960 961 ,62 963 ,64		2SA1048(Y,GR) 2SA933S(Q,R) 2SD882 2SB772	TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR	
C1 0		TUNER UNIT	(X05-3530-12)	
C1 ,2 C3 C4 C5 C6		CK45FF1H103Z CC93FCH1H391J CE04LW1H010M CE04LW1V100M CK45FF1H103Z	CERAMIC 0.010UF Z CERAMIC 390PF J ELECTRO 1.0UF 50WV ELECTRO 10UF 35WV CERAMIC 0.010UF Z	
C7 C8 ,9 C10 C11 ,12 C13 -15		CK45FF1H223Z CK45FF1H103Z CK45FF1H223Z CK45FF1H103Z CE04LW1C470M	CERAMIC 0.022UF Z CERAMIC 0.010UF Z CERAMIC 0.022UF Z CERAMIC 0.010UF Z ELECTRO 47UF 16WV	
C16 C17 C18 C19 C20		CE04LW1H2R2M CE04LW1H3R3M CE04LW1V4R7M CF92FV1H223J CF92FV1H273J	ELECTRO 2.2UF 50WV ELECTRO 3.3UF 50WV ELECTRO 4.7UF 35WV MF 0.022UF J MF 0.027UF J	
C21 C22 C23 C24 C25		CK45FF1H223Z CC45FSL1H101J CE04LW1HR47M CF92FV1H273J CC45FCH1H220J	CERAMIC 0.022UF Z CERAMIC 100PF J ELECTRO 0.47UF 50WV MF 0.027UF J CERAMIC 22PF J	

E: Scandinavia & Europe

K: USA T: England

P: Canada M: Other Aeas

Y: AAFES (Europe) X: Australia

England M:

[⚠] indicates safety critical components.

Y: PX(Far East, Hawaii)
Y: AAFES (Europe)

K: USAT: England

X: Australia

P: Canada M: Other Aeas

[⚠] indicates safety critical components.

* New Parts

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Telle ohne Parts No. werden nicht geliefent.

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No 7

Ref. No.

D4 -6

D4 -6

07

IC1

IC3

01

92 **Q2**

Q3

Q4 Q5 Q5

97

Q7

665

C13

C14

C15

C16 C17 -20

C1 -10

C11 ,12

C21 -24

C25 -28

C29 -32

C33 ,34

C35 -38

C39

C40

C41

C42

C43

C45

C46

C47

C48

C49

C59

C60

C61

C62

C65

C66

C50 -54

C55 ,56

C57 ,58

C63 ,64

C67 -72

, 6

参照番号

Parts No.

部品番号

RD5.1ES(B2)

KV1236(Z2)

25C1923(R.0)

25C1740S(Q,R)

2SC945(A)(Q,P)

2SC1845(F.E)

25C1740S(Q,R)

25C945(A)(Q,P)

25C1740S(Q,R)

2SC945(A)(Q,P)

2SA733(A)(Q.P)

25A933S(Q,R)

W02-0699-05

W02-0700-05

CEO4LW1V4R7M

CEO4LW1C470M

C092FM1H472J

CEO4LW1C470M

CEO4LW1V100M

CEO4LW1V4R7M

CQ92FM1H103J

CF92FV1H433J

CK45FB1H821K

CQ92FM1H102J

CEO4LW1V4R7M

CEO4LW1C470M

CF92FV1H334J

CF92FV1H104J

CF92FV1H333J

CEO4LW1H010M

CQ92FM1H472J

CF92FV1H273J

CEO4LW1C220M

CE04LW1C101M

CQ92FM1H822J

CEO4LW1V100M

CEO4LW1V4R7M

CEO4LW1V4R7M

CE04LW1C470M

CQ92FM1H472J

CEO4LW1C470M

CEO4LW1V100M

CEO4LW1V4R7M

CQ92FM1H103J

CE04LW1V100M

CE04LW1V4R7M

K: USA

CC45FSL1H101J

CC45FSL1H101J

155133

155176

LA1265

LM7001

AN7470

Description

部品名/規格

VARIABLE CAPACITANCE DIQUE

IC(PLL FREQUENCY SYNTHESIZER)

4.7UP

100PF

4700PF

47UF

47UF

10UF

4.7UF

0.010UF

0.043UF

820PF

1000PF

0.33UF

0.10UF

0.033UF

1.0UF

4700PF

22UF

100UF

10UF

4.7UF

100PF

4.7UF

4700PF

47UF

47UF

10UF

4.7UF

10UF

4.7UF

0.010UF

8200PF

0.027UF

4.7UF

47UF

35WV

16WV

16WV

35UV

35WV

35WV

16WV

50WV

16WV

16WV

35WV

35WV

35WV

16WV

16WV

35WV

35WV

35WV

35WV

J

J

ZENER DIODE

IC(FM MPX)

TRANSISTOR TRANSISTOR

TRANSISTOR

TRANSISTOR

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PREAMPLIFIER UNIT (X08-2440-10)

FM FRONT-END ASSY

FM FRONT-END ASSY

IC(FM/AM TUNER)

DIODE

DIODE

位 置 新

Desti- Renation marks 仕 向 備考

MY

MY

KPMY

Ref. No.	Address Ne		Description	Desti- Re-
参照者号	位 選 Sp		部品名/規格	nation mark 仕 向備考
C26 C27 C28 C29 -31 C32		CK45FF1H103Z CE04LW1H010M CC45FCH1H220J CC45FSL1H101J CK45FF1H103Z	CERAMIC 0.010UF Z ELECTRO 1.0UF 50WV CERAMIC 22PF J CERAMIC 100PF J CERAMIC 0.010UF Z	
C33 ,34 C35 C36 ,37 C36 ,37 C38 ,39		CF92FV1H682J CC93FCH1H471J CF92FV1H273J CF92FV1H433J CF92FV1H153J	MF 6800PF J CERAMIC 470PF J MF 0.027UF J MF 0.043UF J MF 0.015UF J	E MY KP MY
C40 C41 -43 C44 C45 C46		CE04LW1H3R3M CE04LW1H2R2M CK45FB1H471K CF92FV1H473J CE04LW1HR47M	BLECTRO 3.3UF 50WV	
C47 C48 C49 C50 ,51 C52 ,53		CE04LW1C470M CE04LW1V100M CE04LW1C470M CE04LW1C220M CC45FSL1H151J	BLECTRO	E E KPMY
C52 ,53 C54 TC1 ,2		CF92FV1H122J CC45FSL1H151J CO5-0303-05	MF 1200PF J CERAMIC 150PF J CERAMIC TRIMMER CAPACITOR(20PF	E E
E2	2D	E20-0321-05	LOCK TERMINAL BOARD (ANTENNA)	
CF1 ,2 CF1 ,2 CF3 CF4		L72-0531-05 L72-0536-05 L72-0099-05 L72-0096-05 L40-1091-17	CERAMIC FILTER CERAMIC FILTER CERAMIC FILTER CERAMIC FILTER CERAMIC FILTER SMALL FIXED INDUCTOR(1.0uH)	KPMY E
L2 L3 L4 L5 L6		L40-1021-14 L40-1091-17 L30-0484-05 L30-0485-05 L79-0125-05	SMALL FIXED INDUCTOR(1.0mH,K) SMALL FIXED INDUCTOR(1.0uH) FM IFT (DISCRIMINATOR) FM IFT (DISTORTION/MONO) LC FILTER	6
L7 L8 L9 L10 X1		L79-0739-05 L31-0509-05 L32-0277-15 L30-0362-05 L77-1122-05	LC FILTER MW-RF COIL (RF ALIGNMENT) MW 0SC COIL(BAND EDGE L) AM IFT (IF TRANSFORMER) CRYSTAL RESONATOR(7.2MHz)	E
R14 R22 ,23 R24 R45 R53		RD14GB2E101J RD14GB2E101J RD14GB2E221J RD14GB2E101J RD14GB2E330J	FL-PROOF RD 100 J 1/4W FL-PROOF RD 100 J 1/4W FL-PROOF RD 220 J 1/4W FL-PROOF RD 100 J 1/4W FL-PROOF RD 33 J 1/4W	E
VR1 VR2 VR3 VR4 VR4		R12-3130-05 R12-3126-05 R12-1089-05 R12-6016-05 R12-8015-05	TRIMMING POT(33K) (FM T-LEVEL) TRIMMING POT(10K) (AM T-LEVEL) TRIMMING POT(4.7K)(VCO) TRIMMING POT(330K)(SEPARATION) TRIMMING POT(1M) (SEPARATION)	E KPMY
S1	2 D	S31-2072-05	SLIDE SWITCH (DEEM, CH SP)	MY
D1 ,2 D1 ,2		1SS133 1SS176 H7S5 1N(B2)	DIQUE DIQUE ZENER DIQUE	

E:	Scandinavia & Europe
Y:	PX(Far East, Hawaii)

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HZS5.1N(B2)

ZENER DIODE

indicates safety critical components.

E:	Scandinavia	å	Europ	e
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Y: PX(Far East, Hawaii)

Y: A

AFES	(Europe)	X:	Austi

East, Hawaii)	T:	England	M: Other Aeas		
(Europe)	X:	Australia		Λ	indicates safety critical components.

K: USA

P: Canada

T: England

M: Other Aeas

Y: AAFES (Europe) X: Australia

P: Canada M: Other Aeas

Les articles non mentionnes dans le Parts No. ne sont pas fournis.

Telle ohne Parts No. werden nicht geliefert.

No.9

Ref. No.	Address		Parts No.	Description	Desti- Re
参照者号	位置	Parts St	* * * *	部品名/規格	nation mar 仕 向 備
C73 C74 C75 C76 C77			CE04LW1C470M CQ92FM1H103J CF92FV1H104J CE04LW1V100M CE04LW1V4R7M	ELECTRO 47UF 16WV MYLAR 0.010UF J MF 0.10UF J ELECTRO 10UF 35WV ELECTRO 4.7UF 35WV	
C78 C79 C81 C82 C83			CQ92FM1H103J CF92FV1H104J CE04LW1C101M CE04LW0J331M CQ92FM1H102J	MYLAR 0.010UF J MF 0.10UF J ELECTRO 100UF 16WV ELECTRO 330UF 6.3WV MYLAR 1000PF J	
C84 C86 C87 ,88 C89 ,90 C91 -94			CE04LW1A101M CE04LW1A470M CC45FSL1H101J CQ92FM1H102J CQ92FM1H103J	ELECTRO 100UF 10WV ELECTRO 47UF 10WV CERAMIC 100PF J MYLAR 1000PF J MYLAR 0.010UF J	
C95 C96 C97 C98 C99			CE04LW1V100M CQ92FM1H102J CE04LW1V100M CE04LW1V4R7M CQ92FM1H102J	ELECTRO 10UF 35WV MYLAR 1000PF J ELECTRO 10UF 35WV ELECTRO 4.7UF 35WV MYLAR 1000PF J	
C100 C102 C105 C106-108 C109			CF92FV1H104J CQ92FM1H102J CF92FV1H104J CQ92FM1H102J CE04LW0J331M	MF 0.10UF J MYLAR 1000PF J MF 0.10UF J MYLAR 1000PF J ELECTRO 330UP 6.3WV	
C110 C111 C112 C113 C114			CF92FV1H104J CE04LW0J331M CF92FV1H104J CE04LW0J331M CF92FV1H104J	MF 0.10UF J ELECTRO 330UF 6.3WV MF 0.10UF J ELECTRO 330UF 6.3WV MF 0.10UF J	
C115 C116 C117 C118 C119			CE04LW0J331M CF92FV1H104J CK45FF1H103Z CE04LW0J331M CE04LW1A101M	BLECTRO	
C120 C121 C122 C123,124 C125-128			CF92FV1H104J CE04LW1A101M CF92FV1H104J CC45FCH1H220J CQ92FM1H103J	MF 0.10UF J ELECTRO 100UF 10WV MF 0.10UF J CERAMIC 22PF J MYLAR 0.010UF J	
C129 C130,131 C132,133 C134-139 C140			CQ92FM1H102J CF92FV1H104J CQ92FM1H102J CC45FSL1H470J CQ92FM1H102J	MYLAR 1000PF J MF 0.10UF J MYLAR 1000PF J CERAMIC 47PF J MYLAR 1000PF J	
C141 C142-144			CC45FSL1H221J CK45FF1H103Z	CERAMIC 220PF J CERAMIC 0.010UF Z	
L1 -7 X1 X2 X3		*	L40-1001-17 L78-0277-05 L78-0244-05 L77-1199-05	SMALL FIXED INDUCTOR(10UH,K) RESONATOR (12MHz) RESONATOR (4MHz) CRYSTAL RESONATOR(18.432MHz)	
CP1 CP2 CP3		*	R90-0482-05 R90-0875-05 R90-0493-05	MULTI-COMP 100KX4 J 1/6W MULTIPLE RESISTOR 100KX5 MULTI-COMP 100KX9 J 1/6W	

E: Scandinavia & Europ	e
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K: USA Y: PX(Far East, Hawaii)

★ indicates safety critical components.

* New Parts

Parts without Parts No. are not supplied.

Les articles non mentionnes dans le Parts No. ne sont pas fournis.

Telle ohne Parts No. werden nicht geliefert.

No.10

Ref. No. 参照番号	Address	Parts	Parts No.	Description 45	nation	33
罗思音 节	位置	5	部品 善号	部品名/規格	仕 向	1
CP4			R90-0864-05	MULTIPLE RESISTOR 100KX14		١
CP5 ,6 CP7			R90-0493-05 R90-0803-05	MULTI-COMP 100KX9 J 1/6W MULTI-COMP 100KX7 J 1/4W	l	l
CP8			R90-0855-05	MULTI-COMP 100KX5 J		l
R17 ,18			RD14NB2E221J	RD 220 J 1/4W		l
R22			RS14KB3D470J	FL-PR00F RS 47 J 2W		l
R24 R27 ,28			RS14KB3D220J RD14NB2E1ROJ	FL-PROOF RS 22		
R27 ,28 R67			RD14NB2E1RUJ	RD 1.0 J 1/4W RD 47 J 1/4W		
R78 ,79			RD14NB2E221J	RD 220 J 1/4W		
R80 ,81			RS14KB3D470J	FL-PROOF RS 47 J 2W		
R83 ,84 R86			RS14KB3D680J	FL-PROOF RS 68 J 2W		
R87			RS14KB3A121J RS14KB3A221J	FL-PR00F RS 120 J 1W FL-PR00F RS 220 J 1W		
R90			RS14KB3A221J	FL-PR00F RS 220 J 1W	1	
D1 -4			HZS13N(B2)	ZENER DIODE		
D1 -4	1		RD13ES(B2)	ZENER DIODE	1	
D5 -13 D5 -13	1		HSS104 1SS133	DIODE DIODE		
IC1			AN1431T	IC(VOLTAGE REGULATOR)		
IC1			TL431CLP	IC(VOLTAGE REGULATOR)		
IC2 IC4	1		NJM78L05A	IC(VOLTAGE REGULATOR/ +5V)		
IC5 -8			TC9213P NJM4565D	IC(2CH ELECTRONIC VOLUME) IC(OP AMP X2)		
IC5 -8			RC4565D	IC(OP AMP X2)		
IC9			LA2730	IC(DOLBY SYSTEM)		
IC10 IC11-14	1		TC9213P NJM4565D	IC(2CH ELECTRONIC VOLUME) IC(OP AMP X2)	1	
IC11-14	1		RC4565D	IC(OP AMP X2)		
IC15,16			M5238P	IC(DUAL OP AMP)		
IC17			CS5326-KP	IC(D/A CONVERTER)		
IC18 IC19		*	TC74HC113AP TC74HC08AP	IC(DUAL J-K FF)		
IC20,21	1		TC74HC74AP	IC(MASTER CLOCK) IC(DUAL D-TYPE FLIP FLOP)		
IC22,23			M5238P	IC(DUAL OP AMP)	1	
IC25,26		*	LC7883K	IC(D/A CONVERTER)		
IC27,28 IC29	İ		LM33464G-12	IC(D-RAM)	1	
IC30		*	LC83010 LC66516B-4677	IC(DSP) IC(DSP u-COM)		1
IC31		*	UPD78214CW-744	IC(AMP u-COM)		
IC32	1		NJM79L05A	IC(VOLTAGE REGULATOR/ +5V)	1	
IC33,34			NJM78L05A	IC(VOLTAGE REGULATOR/ +5V)		
93 .4		ш	2SD1266	TRANSISTOR (X09-3270-10)	L	Ļ
C3 ,4			CEO4LW1V100M	ELECTRO 10UF 35WV	r	Т
C5 .6			CC45FSL1H221J	CERAMIC 220PF J		
C7 ,8 C9 ,10			CK45FB1H102K CE04LW1A101M	CERAMIC 1000PF K	E	
C11 ,12			CF92FV1H123J	ELECTRO	1	
C13 ,14			CF92FV1H332J	MF 3300PF J		
C15 ,16			CEO4LW1V4R7M	ELECTRO 4.7UF 35WV		
C17 -42 C43 ,44	1		CC45FSL1H221J CE04LW1V4R7M	CERAMIC 220PF J ELECTRO 4.7UF 35WV		
C45 ,46			CC45FSL1H101J	ELECTRO 4.7UF 35WV CERAMIC 100PF J	Е	
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E: Scandinavia & Europe

X: Australia

KR-V9030

Y: AAFES (Europe)

T: England X: Australia

P: Canada

M: Other Aeas

K: USA Y: PX(Far East, Hawaii)

T: England

Y: AAFES (Europe)

P: Canada M: Other Aeas

Les articles non mentionnes dans le Parts No. ne sont pas fournis.

Telle ohne Parts No. werden nicht geliefert.

No.11

Ref. No.	Address		Parts No.	Description	Desti- Re-
参照番号	位置	Parts 新	部品番号	部品名/規格	nation mark 仕 由 備考
C47 ,48 C49 ,50 C51 ,52 C53 ,54 C57 ,58			CE04LW1V100M CE04LW1V4R7M CE04LW1V100M CE04LW1V4R7M CK45FB1H471K	ELECTRO 10UF 35W ELECTRO 4.7UF 35W ELECTRO 10UF 35W ELECTRO 4.7UF 35W CERAMIC 470PF K	lv
C59 ,60 C61 ,62 C63 ,64 C65 ,66 C67 ,68			CE04LW1V4R7M CK45FF1H103Z CE04LW1H010M CK45FF1H103Z CF92FV1H104J	ELECTRO 4.7UF 35W CERAMIC 0.010UF Z ELECTRO 1.0UF 50 CERAMIC 0.010UF Z MF 0.10UF J	
C67 ,68 C69 -72 C73 ,74 C75 ,76 C77 ,78			CF92FV1H473J CF92FV1H104J CC45FSL1H220J CC45FSL1H101J CE04JW1H010M	MF 0.047UF J MF 0.10UF J CERAMIC 22PF J CERAMIC 100PF J ELECTRO 1.0UF 50W	KPMY E E
C81 ,82 C83 C84 C85 C86			CK45FB1H471K CK45FF1H103Z CE04LW1H010M CK45FF1H103Z CF92FV1H104J	CERAMIC 470PF K CERAMIC 0.010UF Z ELECTRO 1.0UF 50W CERAMIC 0.010UF Z MF 0.10UF J	ıv E
C86 C87 C88 C89 ,90 C92			CF92FV1H473J CF92FV1H104J CC45FSL1H220J CC45FSL1H101J CE04JW1H2R2M	MF 0.047UF J MF 0.10UF J CERAMIC 22PF J CERAMIC 100PF J ELECTRO 2.2UF 50W	KPMY E E
C93 C94 C95 C96 C97			C90-1398-05 CE04LW1H2R2M C90-1398-05 CE04JW1H2R2M C90-1398-05	NP-ELEC 0.33UF 50M ELECTRØ 2.2UF 50M NP-ELEC 0.33UF 50M ELECTRØ 2.2UF 50M NP-ELEC 0.33UF 50M	1V 1V 1V
C98 C99 C100 C101 C102			CE04LW1H2R2M CE04LW1V100M CK45FF1H103Z CE04LW1C101M CE04LW1H2R2M	ELECTRO 2.2UF 50 ELECTRO 10UF 35 CERAMIC 0.010UF Z ELECTRO 100UF 16 ELECTRO 2.2UF 50 ELECTR	1V
C110-112 C113 C114 C116 C117,118			CK45FB1H102K CK45FB1H471K CE04LW1H010M CE04LW1C101M CE04LW1C221M	CERAMIC 1000PF K CERAMIC 470PF K ELECTRO 1.0UF 50W ELECTRO 100UF 16W ELECTRO 220UF 16W	iv
C119			CEO4LW1V4R7M	ELECTRO 4.7UF 35%	ıv
CN9 J1 J2 J3 J4	2D 2D 2D 2D 2D		E10-0308-05 E13-0634-05 E13-0820-05 E13-0634-05 E13-0446-05	FLAT CABLE CONNECTOR PHONO JACK(PHONO, CD, TAPE) PHONO JACK(TAPE) PLAY, TAF PHONO JACK(DAT, VIDEO1) PHONO JACK(VIDEO2)	REC)
L1 -3			L39-0085-05	PHASE-COMPENSATION COIL	
A B H			N89-3008-45 N89-3008-46 N09-0333-05	BINDING HEAD TAPTITE SCRE BINDING HEAD TAPTITE SCRE TAPPING SCREW (3X12)	
CP1 -3 R107-110 R111-114			R90-0826-05 RD14NB2E220J RD14NB2E221J	MULTIPLE RESISTOR 0.22X2 RD 22 J RD 220 J	1/4W 1/4W

E: Scandinavia & Europe

K: USA

P: Canada

Y: PX(Far East, Hawaii) Y: AAFES (Europe)

T: England

M: Other Aeas

X: Australia

indicates safety critical components.

* New Parts

Parts without Parts No. are not supplied.

Les articles non mentionnes dans le Parts No. ne sont pas fournis.

Telle ohne Parts No. werden nicht geliefert.

No.12

Ref. No.	Address New		Description	Desti- Re
参照者号	位 置 新		部 品 名/規 格	nation ma 仕 向僧
R115-118 R119,120 R121,122 R125,126 R129,130		RD14NB2E2R2J RS14KB3D4R7J RS14KB3A4R7J RD14NB2E822J RS14KB3D4R7J	RD 2.2 J 1/4W FL-PROOF RS 4.7 J 2W FL-PROOF RS 4.7 J 1W RD 8.2K J 1/4W FL-PROOF RS 4.7 J 2W	E
R158,159 R160 R162 R163 R203		RD14NB2E470J RD14NB2E822J RS14KB3D4R7J RS14KB3A4R7J RD14NB2E100J	RD 47 J 1/4W RD 8.2K J 1/4W FL-PROOF RS 4.7 J 2W FL-PROOF RS 4.7 J 1W RD 10 J 1/4W	
R207-209 VR1 -3		RD14NB2E470J R12-1083-05	RD 47 J 1/4W TRIM POT. 1K	
D1 -12 D1 -12 D17 -19 D17 -19 D20 ,21		HSS104A 1SS131 HZS4.7N(B) RD4.7ES(B) HSS104A	DIODE DIODE ZENER DIODE ZENER DIODE DIODE	
D20 ,21 D22 D22 D23 D23		1SS131 HZS5.1N(B2) RD5.1ES(B2) HZS4.7N(B) RD4.7ES(B)	DIODE ZENER DIODE ZENER DIODE ZENER DIODE ZENER DIODE ZENER DIODE	
D24 -26 D24 -26 IC1 IC2 IC3		HSS104A 1SS131 NJM4580D-D TC9163N TC9164N	DIGDE DIGDE IC(GP AMP X2) IC(BILATERAL SWITCH X16) IC(16CH BILATERAL SELECTGR SW)	
IC4 IC5 IC5 IC6 IC7		TC9162N NJM4565D RC4565D NJM4580D-D NJM4565D	IC(ANALOG SWITCH ARRAY) IC(OP AMP X2) IC(OP AMP X2) IC(OP AMP X2) IC(OP AMP X2) IC(OP AMP X2)	
IC7 IC8 IC9 Q1 -4 Q5 ,6		RC4565D TA8409S NJM4580D-D 2SC2878(B) 2SC4137(V,W)	IC(OP AMP X2) IC(MOTOR CONTROL) IC(OP AMP X2) TRANSISTOR TRANSISTOR	
97 ,8 99 ,10 911 ,12 913 ,14 915 ,16		2SC3944A(Q,R) 2SA1535A(Q,R) 2SC2922*5 2SA1216*5 2SC1845(F,E)	TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR	
Q17 Q18 Q19 Q21 ,22 Q26		2SC4137(V,W) 2SD2222*5 2SB1470*5 2SC2878(B) 2SC1845(F,E)	TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR	
927 -31 927 -31 932		2SA1048(Y,GR) 2SA933S(Q,R) 2SC2003(L,K)	TRANSISTOR TRANSISTOR TRANSISTOR	
	S		UNIT (X13-6930-10)	
C1 -3 C4 C5	*	CEO4LW1V4R7M	ELECTRO 4.7UF 35WV ELECTRO 4.7UF 16WV NP-ELEC 22UF 10WV	

E: Scandinavia & Europe

K: USA

P: Canada

Y: PX(Far East, Hawaii) Y: AAFES (Europe)

T: England X: Australia M: Other Aeas

★ indicates safety critical components.

Les articles non mentionnes dans le Parts No. ne sont pas fournis.

No.13

Ref. No.		New Parts No.	Description	Desti- Re-
参照番号		Perts 新 部品番号	部品名/規格	nation mar 仕 向 備4
6 ,7		CE04LW1C220M	ELECTRO 22UF 16WV	
116 ,17		RD14NB2E391J	RD 390 J 1/4W	
)1 -5)1 -5		HSS104 1SS133	DIQDE DIQDE	
6 .7		HZS6.8N(B2)	ZENER DIODE	
6 .7 C1 .2		RD6.8ES(B2) NJM4565D-A	ZENER DIODE IC(OP AMP X2)	
)1		2SC2878(A,B)	TRANSISTOR	
	•	DISPLAY U	NIT (X14-3010-10)	
027	Т	B30-0431-05	LED(LN21CPH)(STAND-BY)	T
21		CE04LW1H010M	ELECTRO 1.OUF 50WV	
2		CK45FF1H223Z	CERAMIC 0.022UF Z	
3 3		CE04LW1A101M C90-1827-05	BACKUP 0.047F 5.5WV	
5		CE04LW1A101M	BACKUP 0.047F 5.5WV ELECTRO 100UF 10WV	
6		CK45FF1H103Z	CERAMIC 0.010UF Z	
7 ,8 9 -14	1	CC45FCH1H101J CC45FSL1H221J	CERAMIC 100PF J CERAMIC 220PF J	
15	1 1	CEO4LW1A101M	ELECTRO 100UF 10WV	i i
16		CK45FF1H103Z	CERAMIC 0.010UF Z	
17 ,18 19 -24		CC45FCH1H101J CC45FSL1H221J	CERAMIC 100PF J CERAMIC 220PF J	
25	1 1	CEO4LW1A101M	ELECTRO 100UF 10WV	1
26 27		C91-0749-05 CC45FSL1H221J	CERAMIC 220PF K CERAMIC 220PF J	
28 ,29		CEO4LW1H100M	ELECTRO 10UF 50WV	
101,102		CQ92FM1H122J	MYLAR 1200PF J	
103,104		CF92FV1H333J	MF 0.033UF J	1
106		CF92FV1H104J C90-1333-05	MF 0.10UF J NP-ELEC 22UF 10WV	
111,112		CE04CW1H010M	ELECTRO 1.OUF 50WV	
113,114	1 1	CF92FV1H473J	MF 0.047UF J CERAMIC 220PF J	
116		CC45FSL1H221J C91-0749-05	CERAMIC 220PF K	
117		CC45FSL1H221J	CERAMIC 220PF J	
118 119		C91-0749-05 CC45FSL1H470J	CERAMIC 220PF K CERAMIC 47PF J	
120		C91-0737-05	CERAMIC 47PF J	1
121-124		CEO4CW1H010M	ELECTRO 1.OUF 50WV	
125-128		CE04CW1C100M	ELECTRO 10UF 16WV	
129,130		CE04JW1C220M CK45FF1H103Z	ELECTRO 22UF 16WV CERAMIC 0.010UF Z	
1	2A	E13-0311-05	PHONO JACK (VIDEO4)	
2	2A	E06-0821-05	CYLINDRICAL RECEPTACLE(SVIDEO)	
1 2 ,3		L78-0267-05 L78-0274-05	RESONATOR (4.1943MHz) RESONATOR (600kHz)	
P1	1 1	R90-0482-05	MULTI-COMP 100KX4 J 1/6W	
P2 ,3		R90-0856-05	MULTI-COMP 10KX5 J	
:P4 :P5		R90-0815-05	MULTI RESISTOR 10KX7	
AT J	1 1	R90-0805-05	MULTI-COMP 10KX8 J 1/4W	1

E: Scandinavia & Europe

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Y: AAFES (Europe) X: Australia

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* New Parts

Parts without Parts No. are not supplied.

Les articles non mentionnes dans le Parts No. ne sont pas fournis.

Telle ohne Parts No. werden nicht geliefert.

No.14

Ref. No.	Address	New Parts	Parts No.	Description		Re-
参照番号	位置	Si Si	部品番号	部品名/規格	nation m	mari 備考
R111,112 VR1 VR2 VR3 VR4 ,5	2C 3B 3B 1B	* *	RD14NB2E221J R29-5049-05 R06-5134-05 R06-5186-05 R10-3044-15	RD 220 J 1/4W POTENTIOMETER(VOLUME CONTROL) POTENTIOMETER(BALANCE) POTENTIOMETER(LOUDNESS) POTENTIOMETER(TREBLE, BASS)		
S1 -48	1 A		S40-1064-05	PUSH SWITCH		
D1 -24 D1 -24 D25 D25 D26			HSS104 1SS133 HZS3.9N(B2) RD3.9ES(B2) HZS4.7N(B)	DIODE DIODE ZENER DIODE ZENER DIODE ZENER DIODE ZENER DIODE		
026 0141-146 0141-146 ED1 IC1	18	*	RD4.7ES(B) HSS104 1SS133 16-MT-29GK XRM9021A	ZENER DIODE DIODE DIODE FLUORESCENT INDICATOR TUBE IC(ROM)		
IC2 IC3 ,4 IC5 IC5		*	UPD75116CW-179 UPD7537ACU-220 NJM4565D-D RC4565D-D 2SA1048(Y,GR)	IC(MICROPROCESSOR) IC(MICROPROCESSOR) IC(OP AMP X2) IC(OP AMP X2) TRANSISTOR		
91 92 92 93 -12 93 -12			2SA933S(Q,R) 2SC1740S(Q,R) 2SC2458(Y,GR) DTA124ES RN2203	TRANSISTOR TRANSISTOR TRANSISTOR DIGITAL TRANSISTOR DIGITAL TRANSISTOR		
Q13 -15 Q13 -15 Q16 -19 Q16 -19			2SC1740S(Q,R) 2SC2458(Y,GR) DTA124ES RN2203	TRANSISTOR TRANSISTOR DIGITAL TRANSISTOR DIGITAL TRANSISTOR		
A1	1B		W02-0975-05	ELECTRIC CIRCUIT MODULE		
C1 ,2	PO	W	ER AMPLIFIE			_
C1 ,2 C3 -6 C7 ,8 C9 -12 C13 ,14			CE04LW1H010M CC45FSL1H101J CE04LW1A101M CK45FF1H103Z CC45FSL1H220J	ELECTRO		
C15 ,16 C17 ,18 C19 ,20 C21 C22			CC45FSL1H150J CC45FSL1H221J CC45FSL1H070D CE04LW2A220M CE04LW2A101M	CERAMIC 15PF J CERAMIC 220PF J CERAMIC 7.0PF D ELECTRO 22UF 100WV ELECTRO 100UF 100WV		
C23 C25 ,26 C31 C32 ,33 C34			CE04LW1V100M CC45FSL1H101J CE04LW1H010M CC45FSL1H101J CE04LW1A470M	ELECTRO 10UF 35WV CERAMIC 100PF J ELECTRO 1.0UF 50WV CERAMIC 100PF J ELECTRO 47UF 10WV		
C35 C36 C37 C38 C41			CC45FSL1H470J CC45FSL1H221J CC45FSL1H020C CC45FSL1H470J CE04LW1H010M	CERAMIC 47PF J CERAMIC 220PF J CERAMIC 2.0PF C CERAMIC 47PF J ELECTR0 1.0UF 50WV		
	1					
C42	İ		CC45FSL1H221J	CERAMIC 220PF J	1 1	

E: Scandinavia & Europe

K: USA

P: Canada M: Other Aeas

Y: PX(Far East, Hawaii)
Y: AAFES (Europe)

T: England X: Australia

♠ indicates safety critical components.

PARTS LIST

× New Parts

Parts without Parts No. are not supplied.

Les articles non mentionnes dans le Parts No. ne sont pas fournis.

No 15

Ref. No.	Address New		Description	Desti- Re
参照者号	位置 新		部 品 名/規 格	nation mar 仕 南僧
243 244 245 246 247		CC45FSL1H101J CE04LW1A470M CC45FSL1H470J CC45FSL1H221J CC45FSL1H020C	CERAMIC	
348 ,49 350 351 ,52		CE04LW1V470M CC45FSL1H470J CE04LW2A010M	ELECTRO 47UF 35WV CERAMIC 47PF J ELECTRO 1.0UF 100WV	
115 -18 119 ,20 127 -30 131 ,32	*	R92-1742-05 RD14NB2E151J RD14NB2E221J RD14NB2E470J RD14NB2E151J	CARBON FILM RESISTOR 2.2K RD 150 J 1/4W RD 220 J 1/4W RD 47 J 1/4W RD 150 J 1/4W	
151 ,52 167 170 ,71 172 ,73		RD14NB2E221J RD14NB2E151J RD14NB2E221J RD14NB2E470J	RD 220 J 1/4W RD 150 J 1/4W RD 220 J 1/4W RD 47 J 1/4W	
01 -3 01 -3 04 04 06 ,7		HSS104 1SS133 HZS5.1S(B2) RD5.1JS(B2) HSS104	DIODE DIODE ZENER DIODE ZENER DIODE DIODE	
06 ,7 21 -4 25 -10 25 -10 211 -14		1SS133 2SA992(F,E) 2SA1048(Y,GR) 2SA933S(Q,R) 2SC2631(R,S)	DIODE TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR	
915 ,16 921 ,22 923 ,24 925 931 ,32		2SA1123(R,S) 2SA992(F,E) 2SC2631(R,S) 2SA1123(R,S) 2SA992(F,E)	TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR	
33 ,34 35		2SC1845(F,E) 2SA992(F,E)	TRANSISTOR Transistor	

E: Scandinavia & Europe Y: PX(Far East, Hawaii)

K: USA

P: Canada M: Other Aeas

Y: AAFES (Europe)

T: England X: Australia

⚠ indicates safety critical components.

SPECIFICATIONS

 \triangle Caution: Read this page carefully to ensure safe operation.

(For U.S.A.	and	Canada)
Audio section		

Rated power output at the STEREO operation

120 watts per channel minimum RMS, both channels driven at 8 Ω , from 20 Hz to 20,000 Hz with no more than 0.03% total harmonic distortions. (FTC)

AC outlets...... switched × 3, total 200 W, 1.6 A max.

(For other countries)

A ultranslan
Audio section
Rated power output at the STEREO operation (IHF '66) from 20 Hz to 20 kHz, 0.06% T.H.D., at 8 Ω
Power output at the Surround operation Front (1 kHz, 0.9% T.H.D. at 8 Ω) 75 W + 75 W Center (1 kHz, 0.9% T.H.D. at 8 Ω) 75 W Rear (1 kHz, 0.9% T.H.D. at 8 Ω) 20 W + 20 W
Total harmonic distortion (1 kHz, 8 Ω) 0.03% at 65 W Frequency response CD 10 Hz~50 kHz, +0 dB, -3 dB
$\begin{array}{llllllllllllllllllllllllllllllllllll$
Loudness control at -30 dB VOLUME level +8 dB (100 Hz), +2 dB (10 kHz) max.
$\begin{tabular}{lllllllllllllllllllllllllllllllllll$
PM Towns and in
FM Tuner section
Tuning frequency range

Note:

KENWOOD follows a policy of continuous advancements in development. For this reason specifications may be changed without notice.

KR-V9030

KENWOOD follows a policy of continuous advancements in development. For this reason specifications may be changed without notice.

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Component and circuitry are subject to modification to insure best operation under differing local conditions. This manual is based on the U.S.A. (K) standard, and provides information on regional circuit modification through use of alternate schematic diagrams, and information on regional component variations through use of parts list.

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